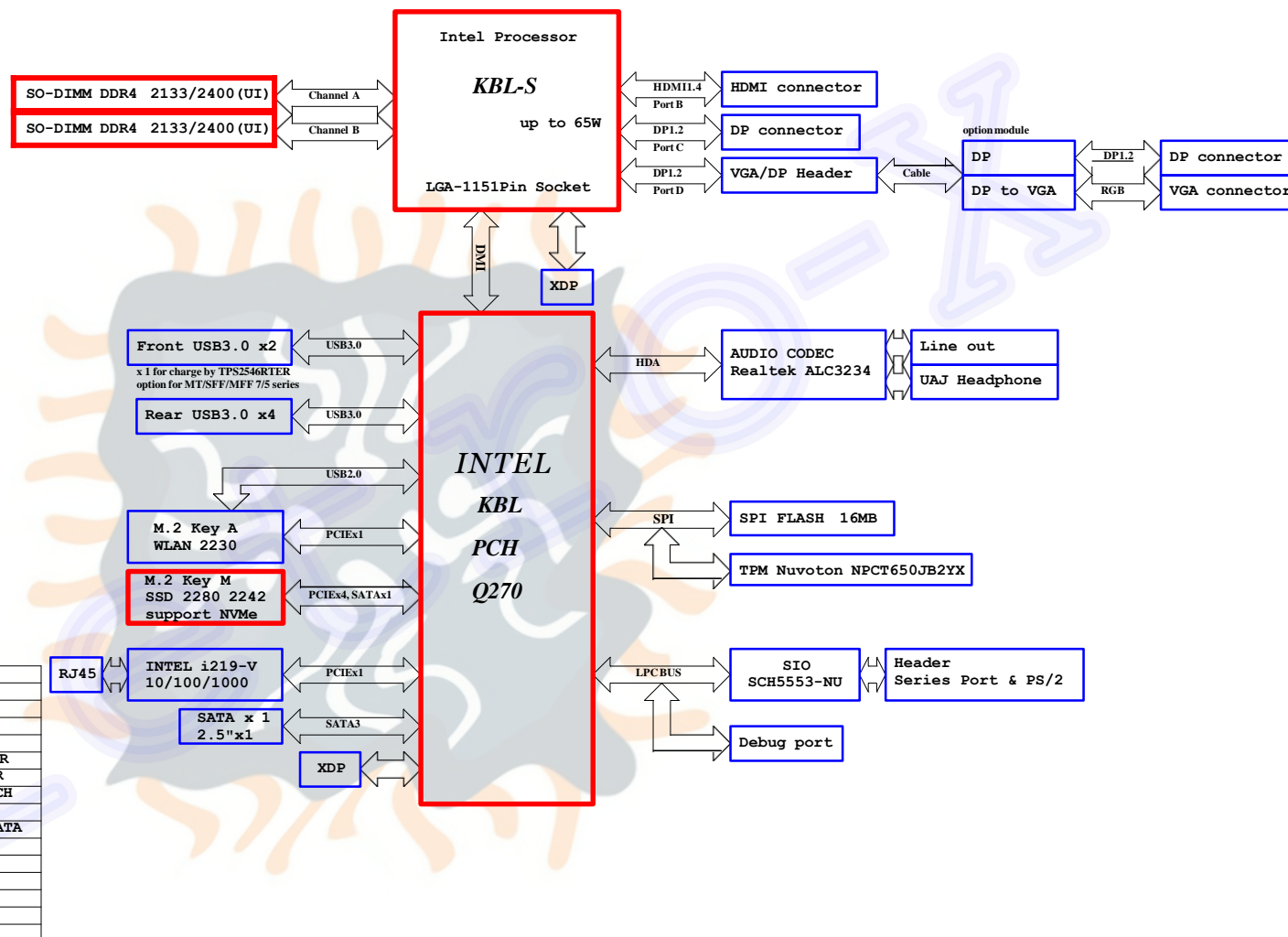


# MFF-AF

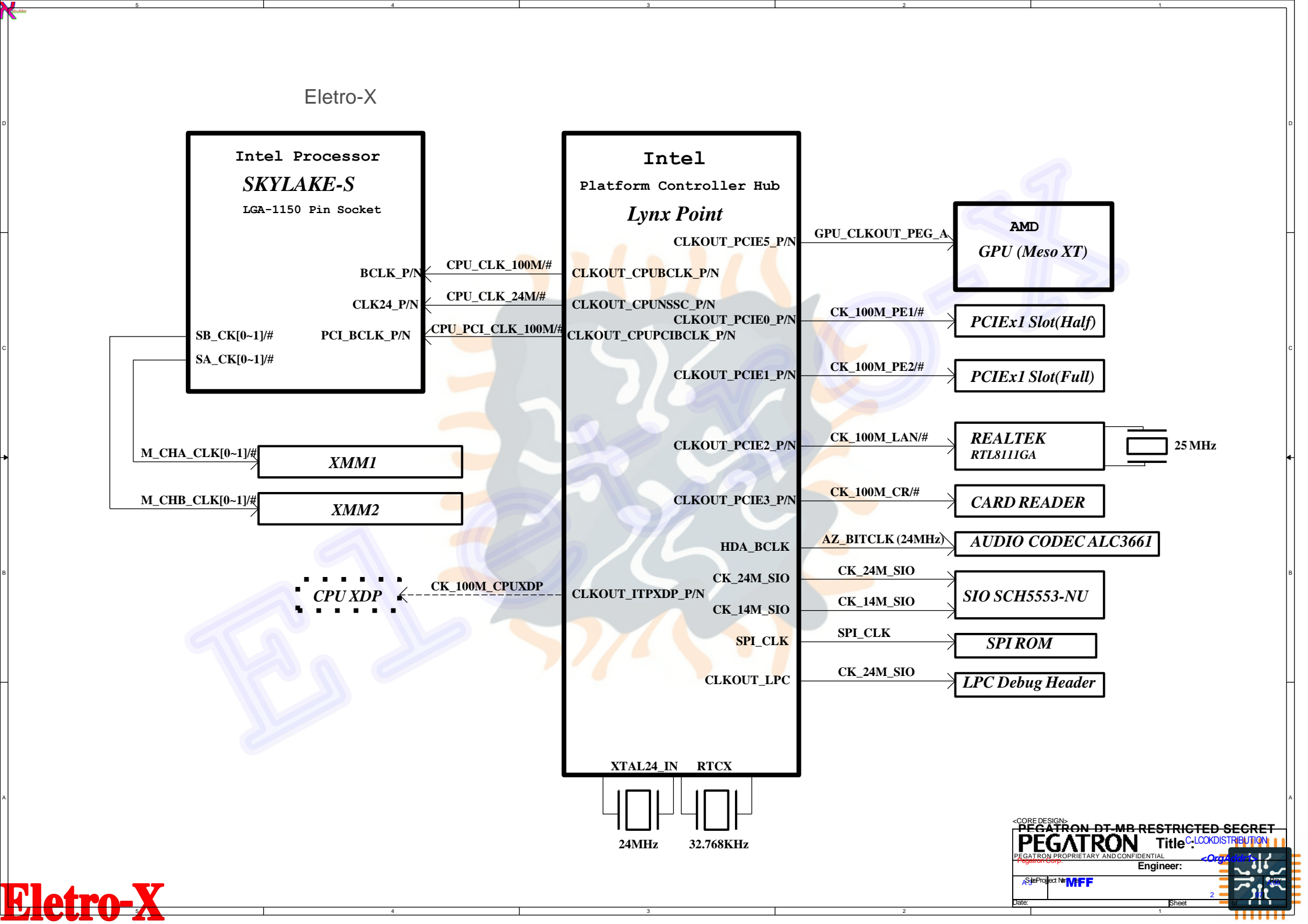
PAGE	TITLE
01	BLOCK DIAGRAM
02	CLOCKS DISTRIBUTION
03	SIGNAL & RESET MAP
04	CHANGE HISTORY
05	POWER FLOW 35W
06	POWER DISTRIBUTION
07	POWER SEQUENCE
08~13	CPU LGA1151
14	DDR4 SODIMM CH-A
15	DDR4 SODIMM CH-B
16	DDR4 TERMINATION A&B
17~24	INTEL PCH
25	SM BUS Control
26	SPI ROM
27	ME DISABLE
28	NGFF KEY A 2230 (WLAN)
29	NGFF KEY M 2280 (SSD)
30	LOM Intel i219
31	LAN JACK
32	N/A
33	Front USB3.0
34	N/A
35	Front 3.0 USB CHARGE IC
36	REAR USB3.0
37	REAR USB3.0
38	N/A
39	TYPEC CYPD2122
40	TYPEC CONN
41	PS2 and Comport header
42	VGA option header
43	REAR LINE OUT& GHS CONN
44	N/A
45	SIO SCH5553V-NU
46	SCREW HOLE
47	FAN CIRCUIT
48	intruder
49	DEBUG LED
50	APS/LPC DEBUG
51	RTC
52	SATA HDD CONN & LED
53	HDMI OUT
54	AUDIO CODEC ALC3234
55	PWRBTN & LED
56	N/A
57	N/A
58	TPM Z32H320TC
59	TPM NECT650JAAYX
60	DP CONN
63~64	DC IN
65	PCHPWROK VCCSTPG
68	VCCSA & VCCIO
69	5VSB/+5VA/+3P3VA
70	+3P3VSB/WLAN

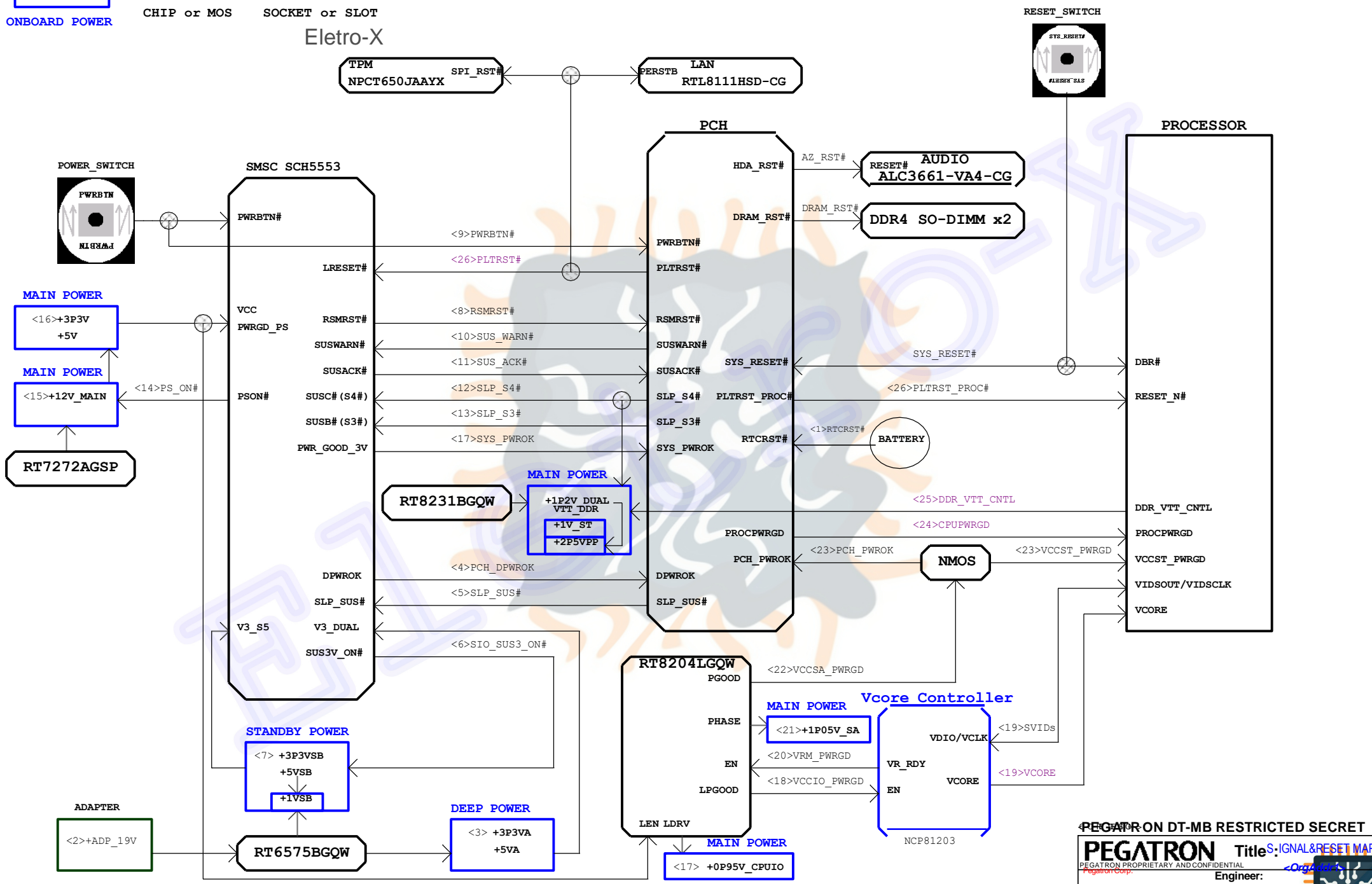
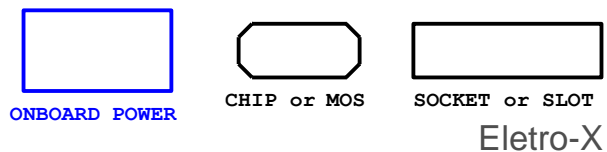
Eletro-X  
Revision: A00

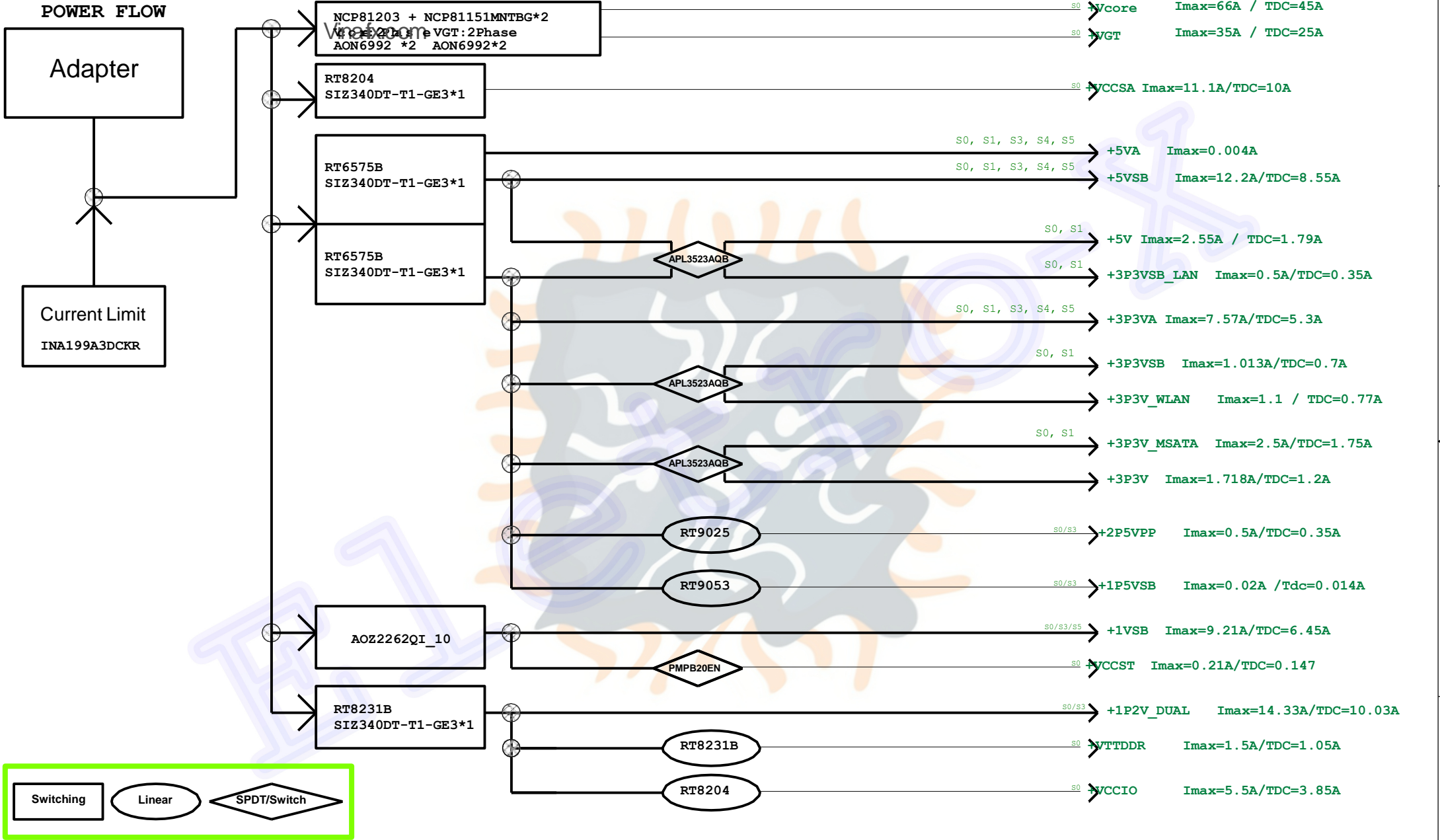


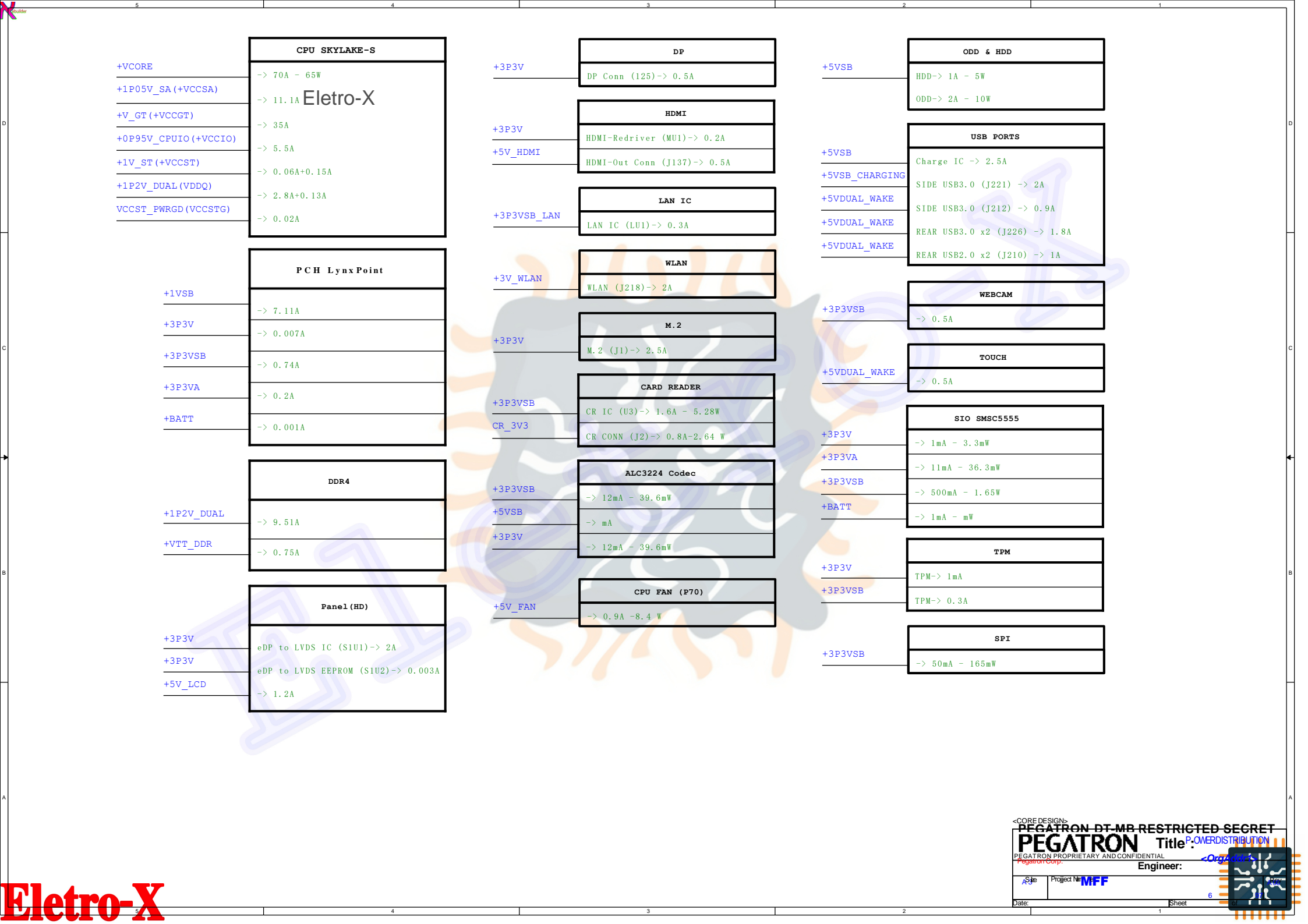
PEGATRON DT-MB RESTRICTED SECRET

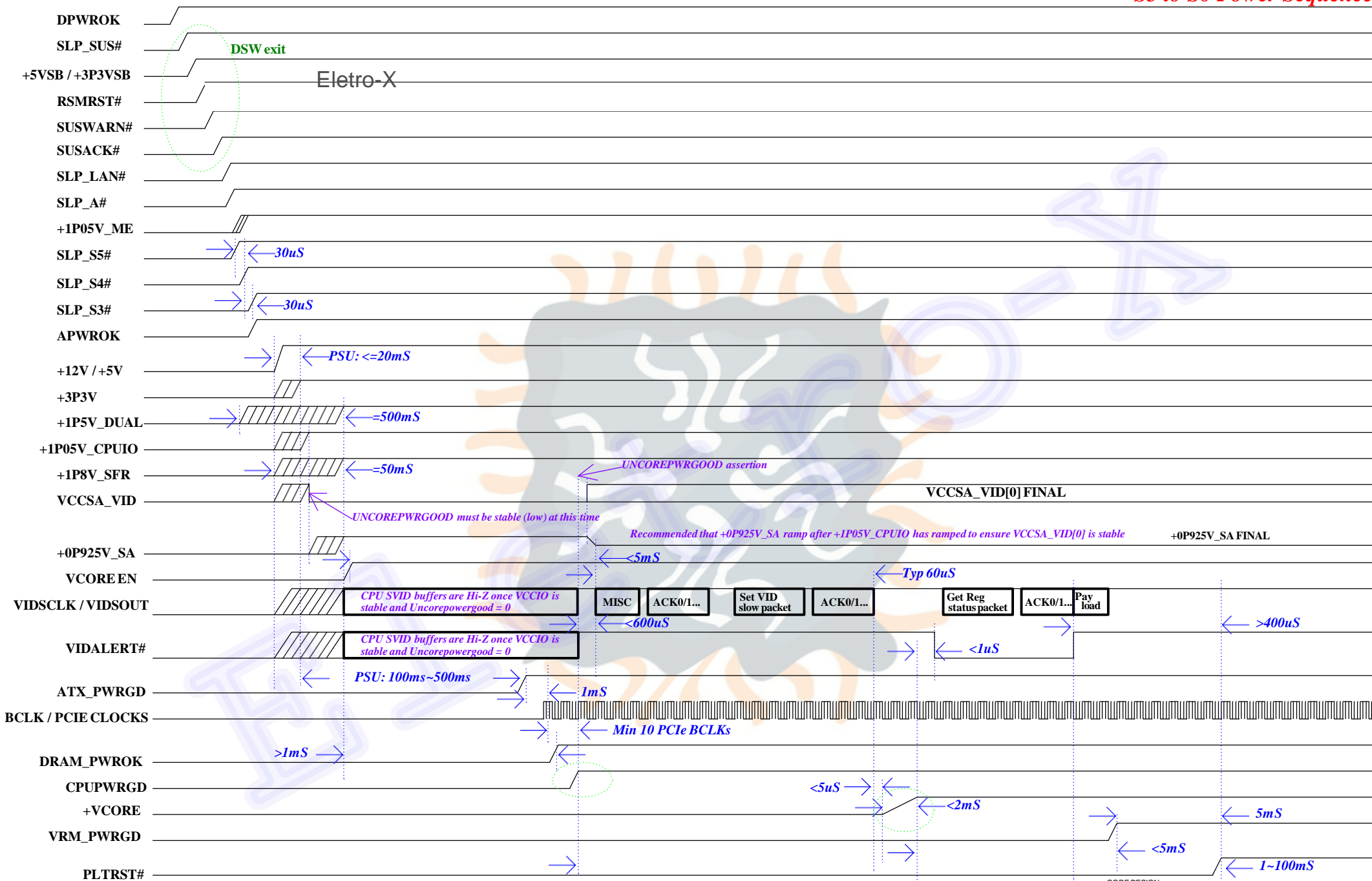
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PEGATRON PROPRIETARY AND CONFIDENTIAL	
Revision: 1.0	Engineer: MFF
Design Name: MFF	Project: MFF
Client: MFF	Sheet: 1



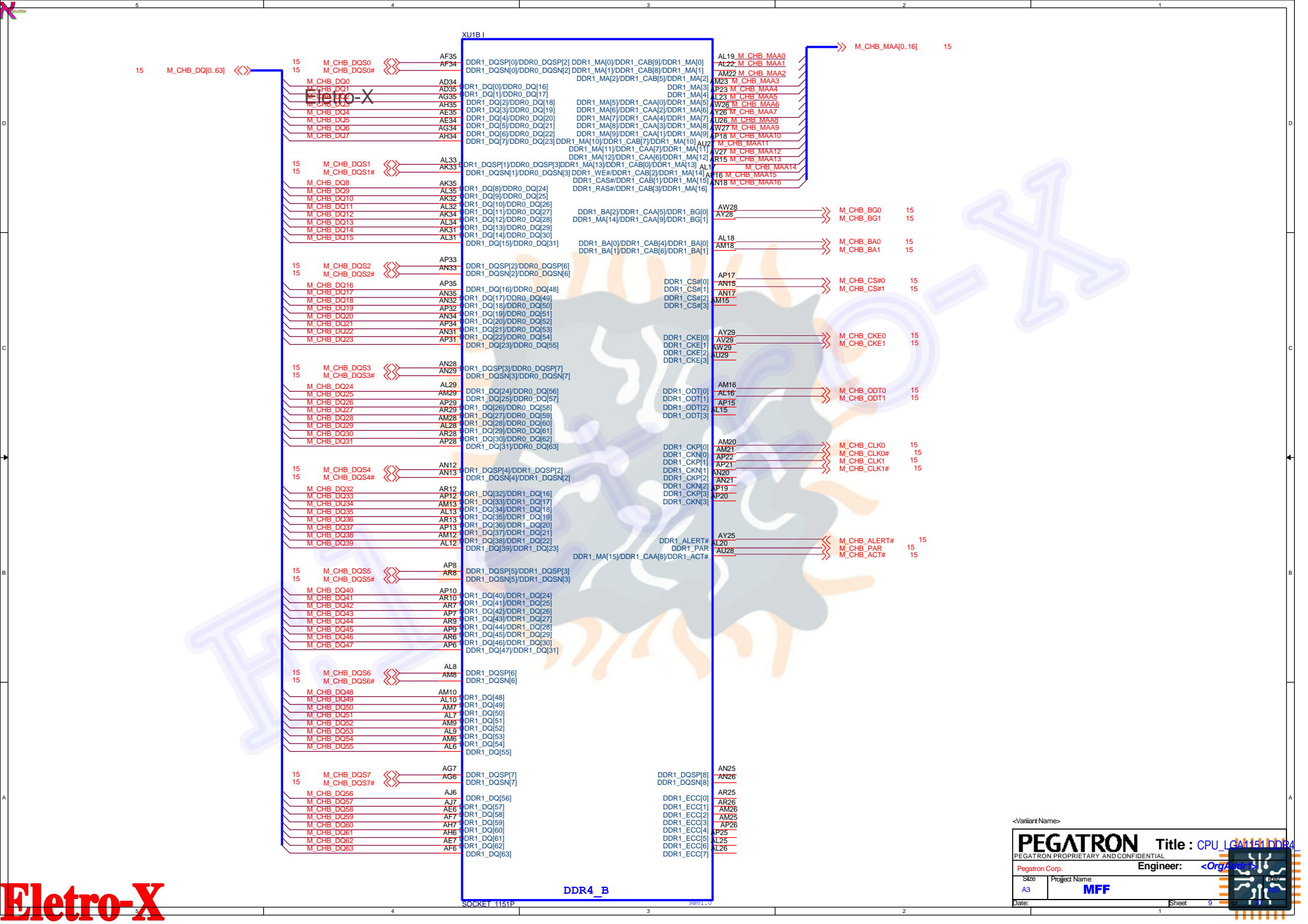


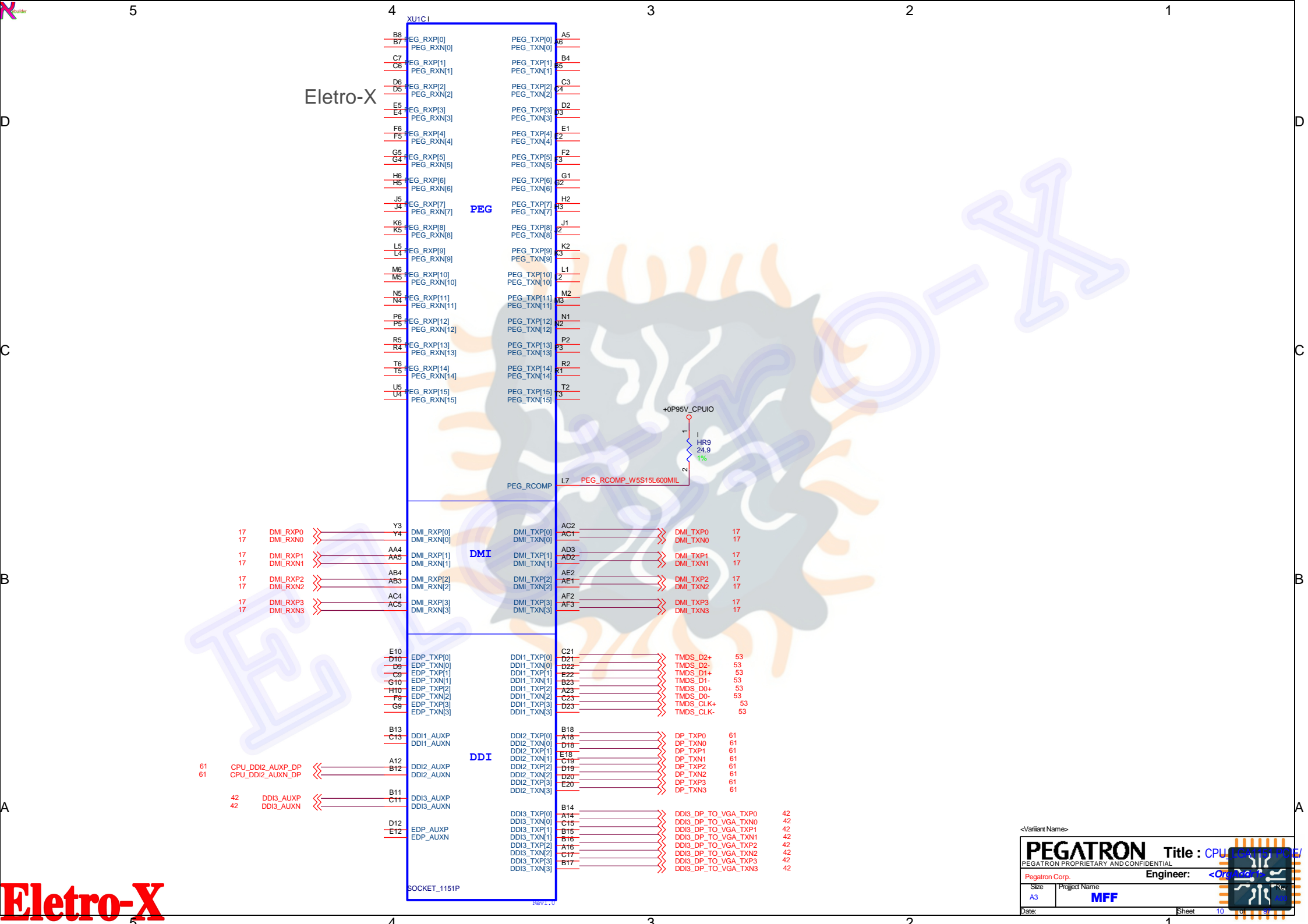












Eletron-X

PEG

DMI

DDI

SOCKET\_1151P

REV1.1

<Variant Name>

**PEGATRON** Title : CPU\_DDI2\_AUXP\_DP  
PEGATRON PROPRIETARY AND CONFIDENTIAL

Pegatron Corp. Engineer: <Org>

Size Project Name  
A3 MFF

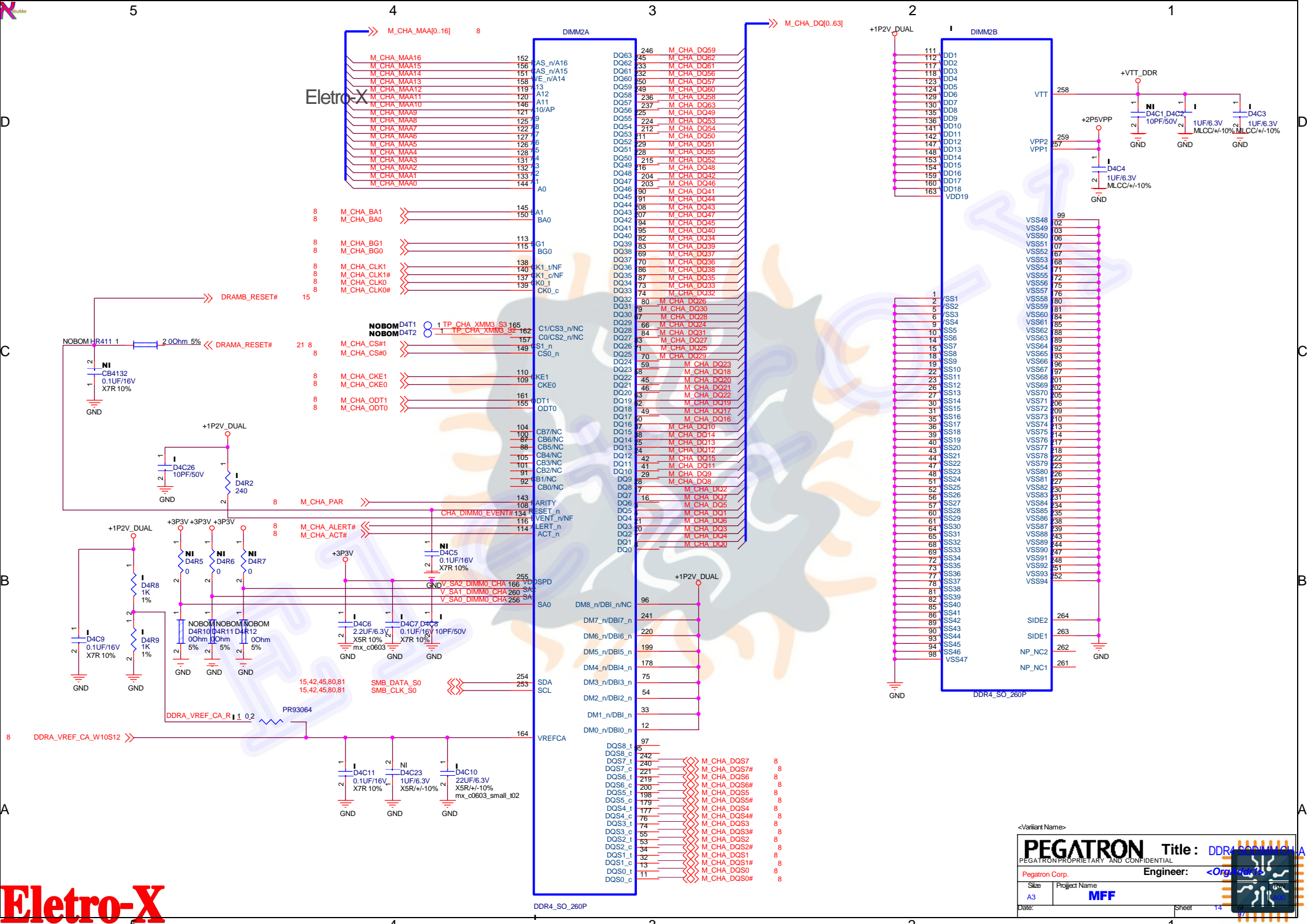
Date: Sheet 10 of 10

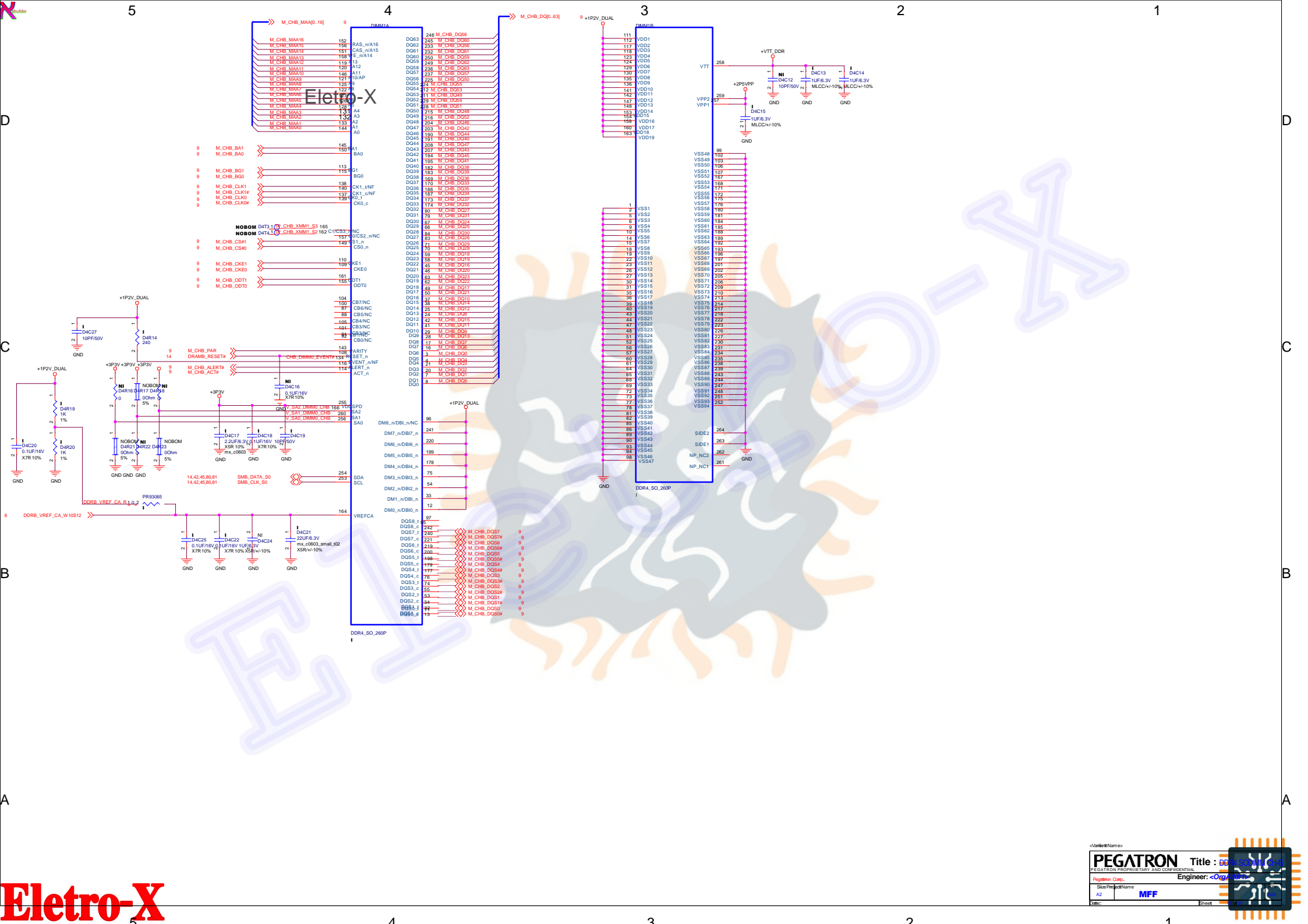


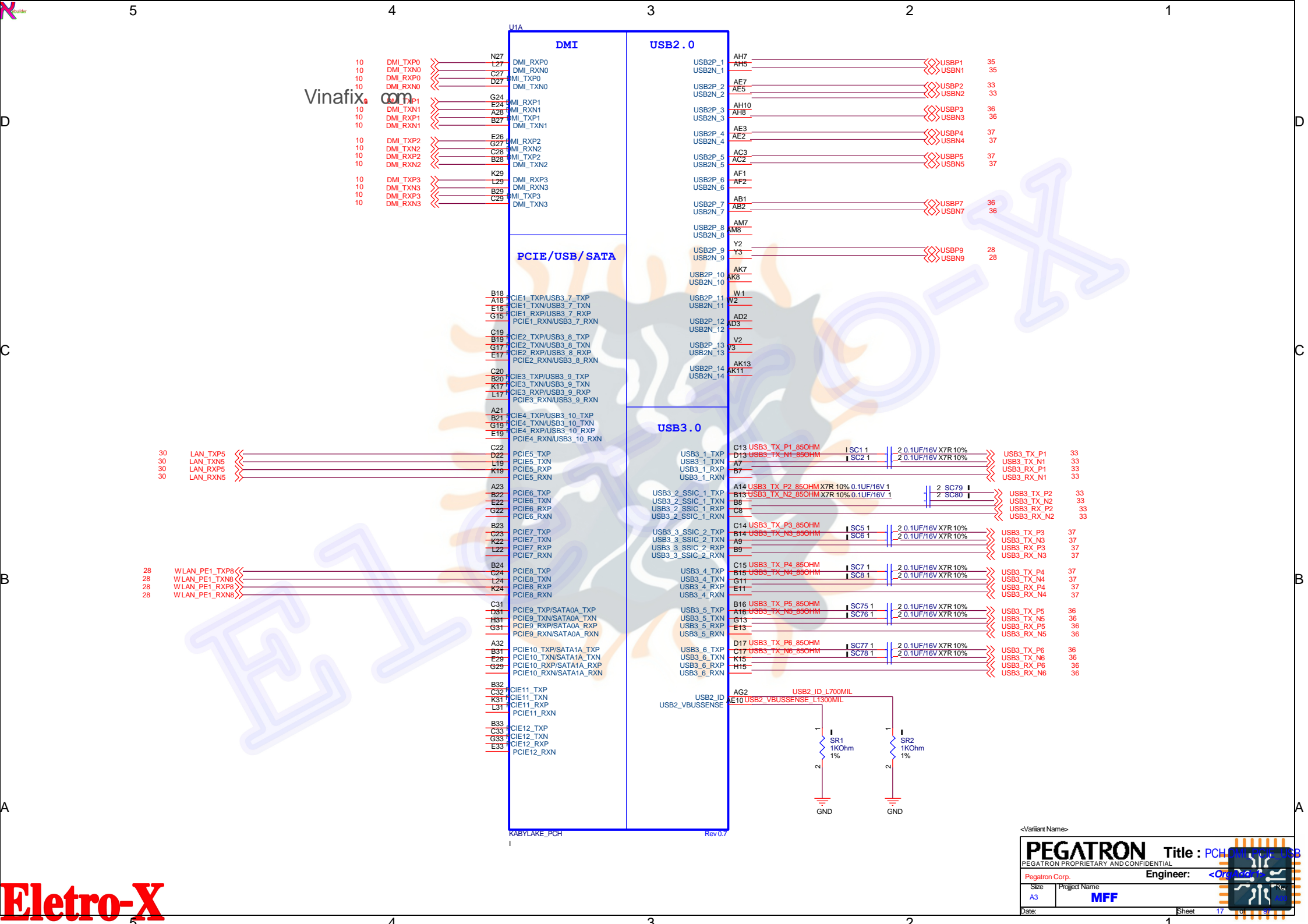


P

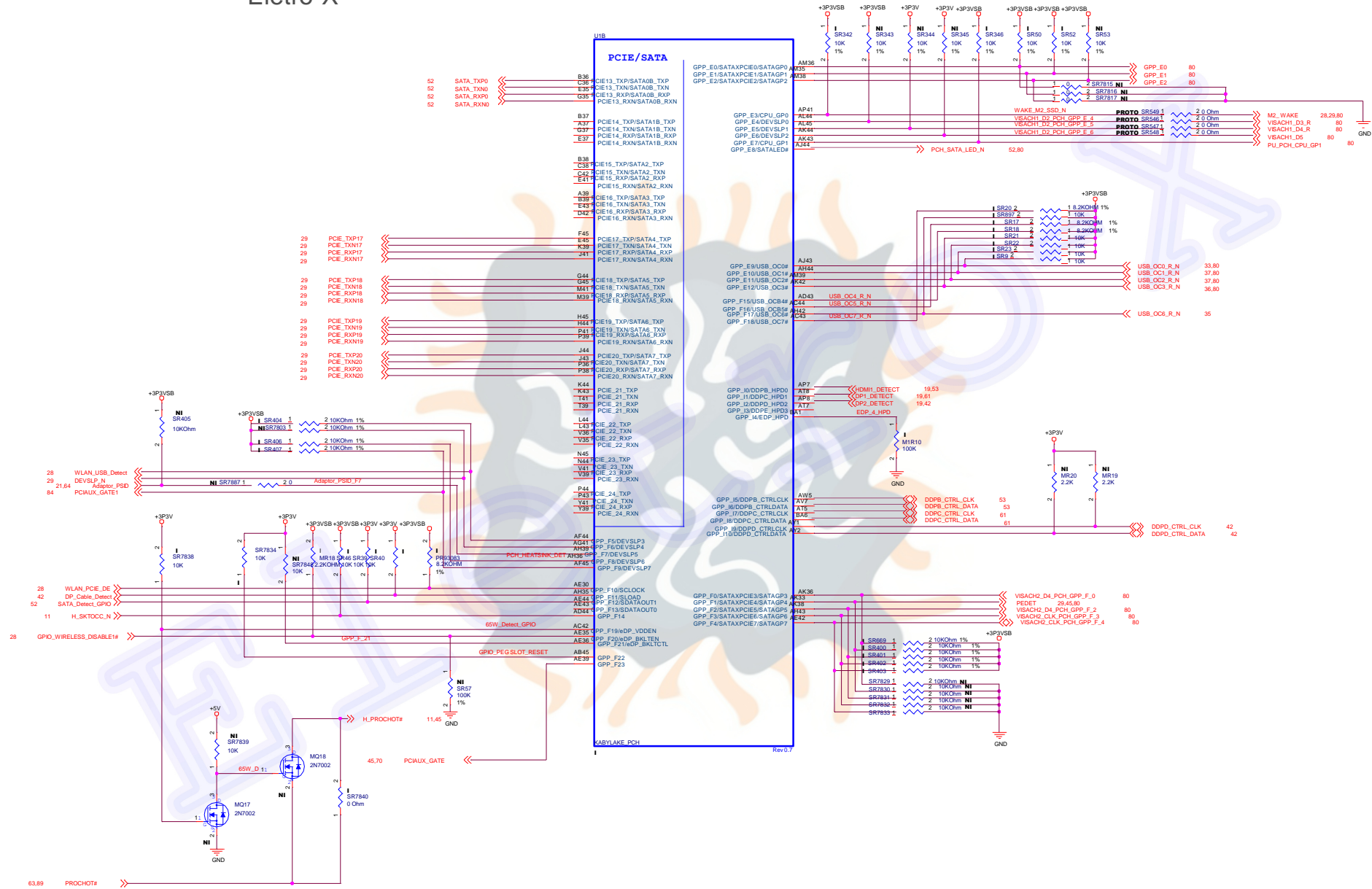




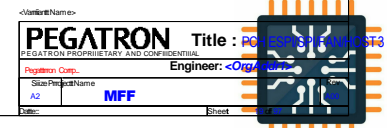




## Eleto-X



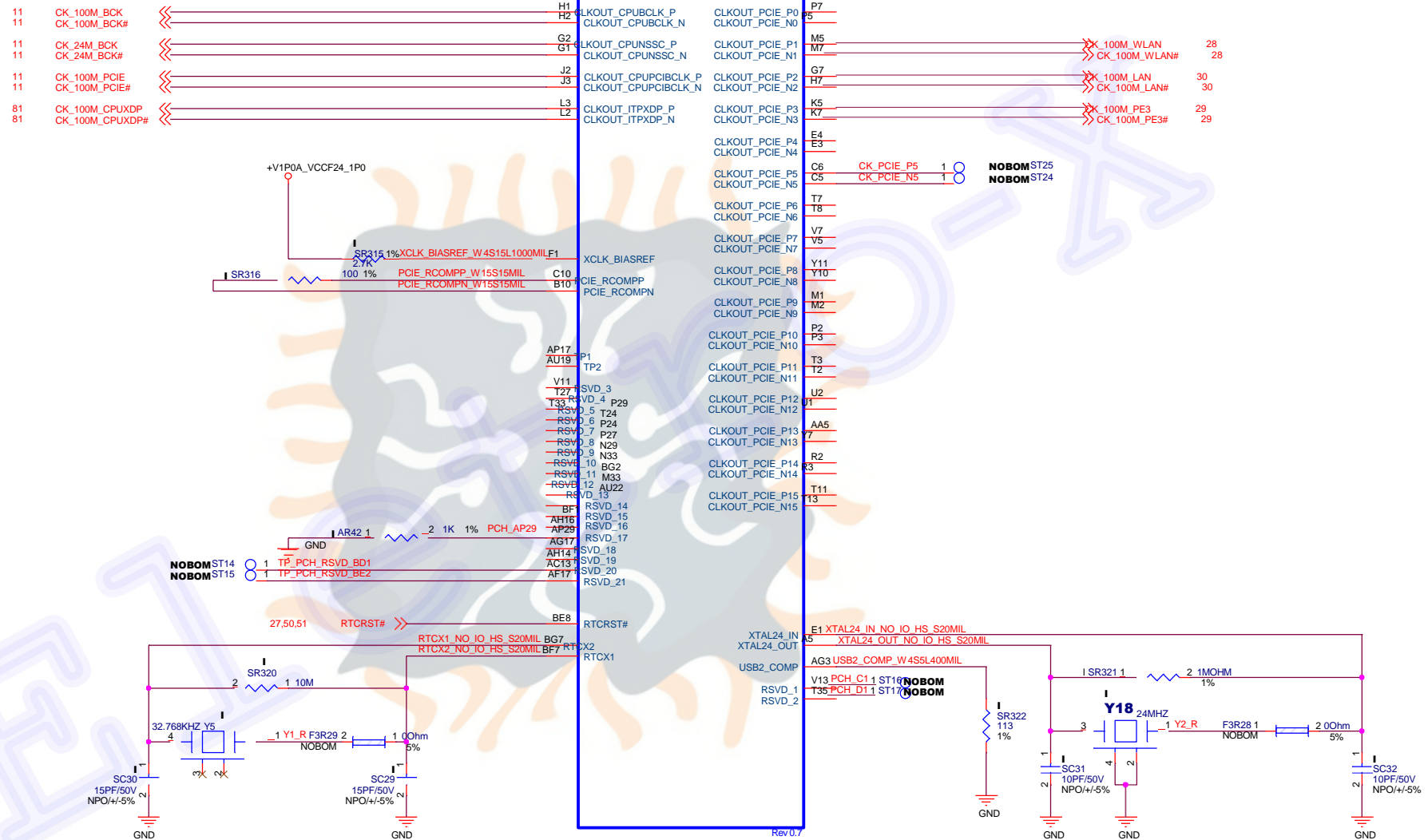
# Eletro-X





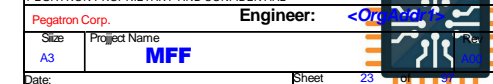


## Eletro-X



&lt;Variant Name&gt;

<b>PEGATRON</b> Title: PCH	
PEGATRON PROPRIETARY AND CONFIDENTIAL	
Pegatron Corp.	Engineer: <Orion>
Size A3	Project Name MFF
Date:	Sheet 22



D

D

C

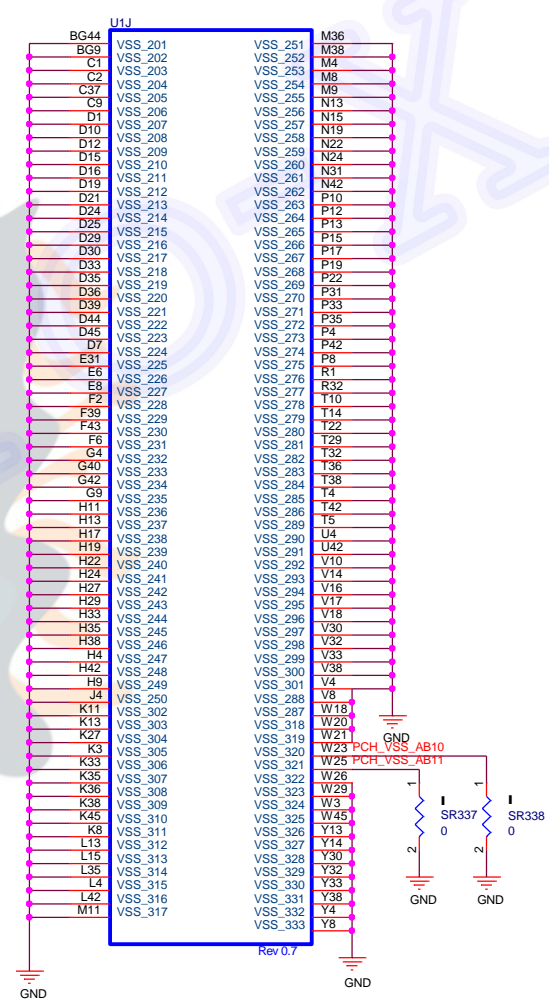
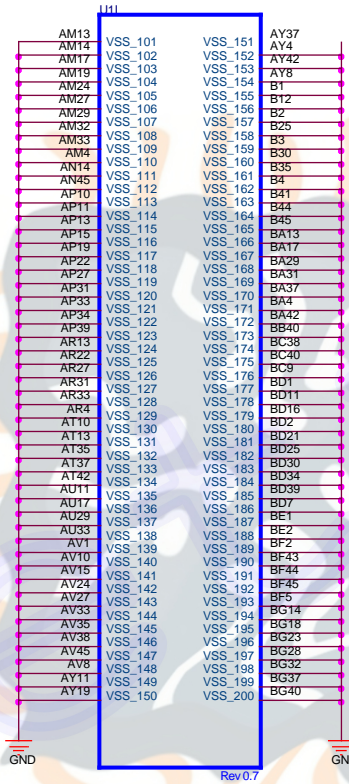
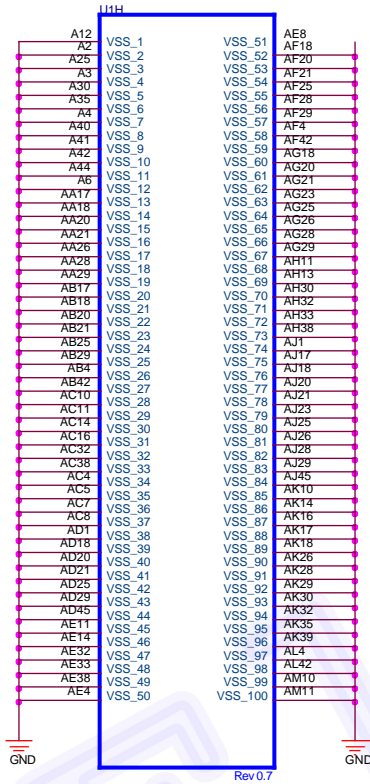
C

B

B

A

A



<Variant Name>

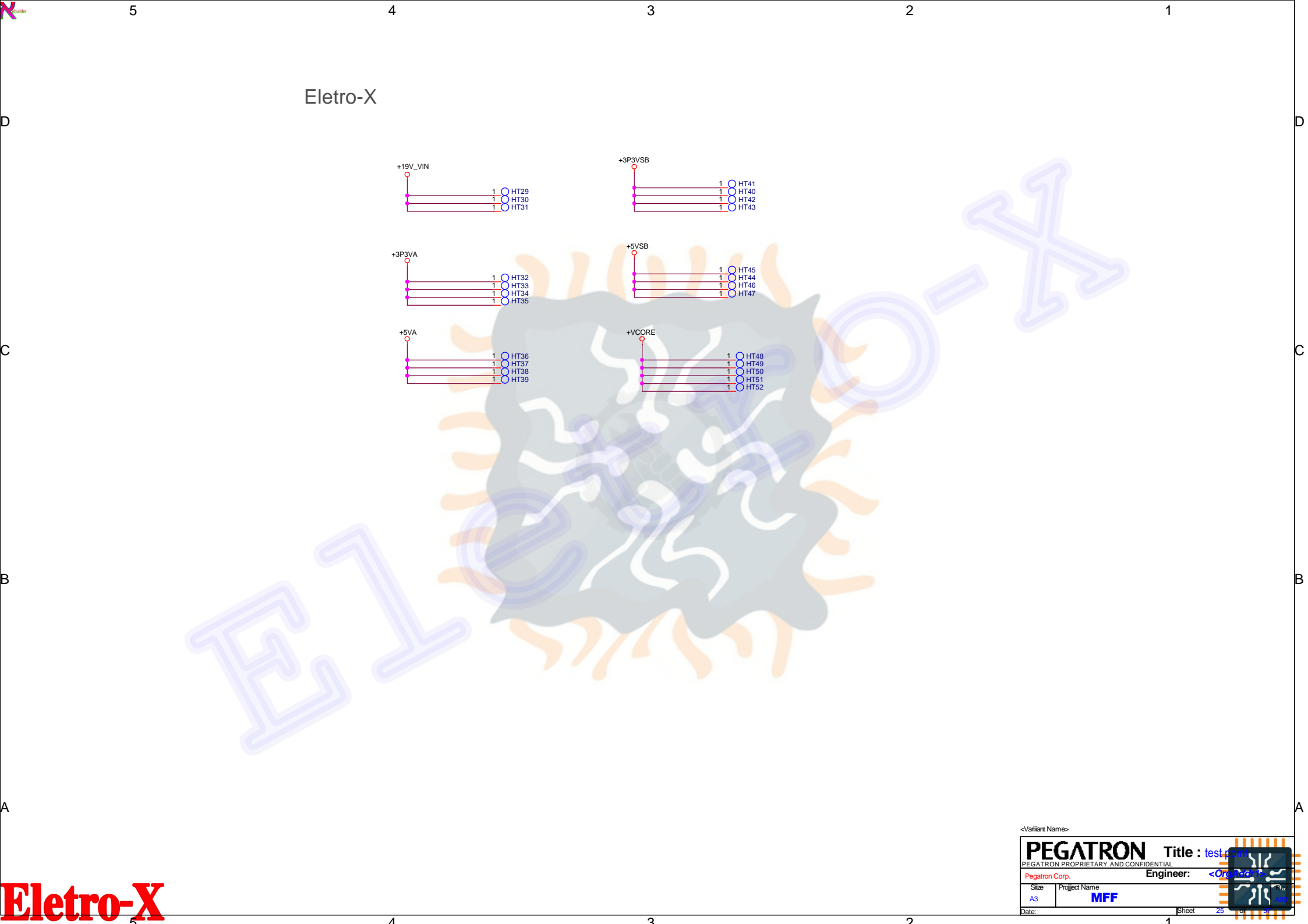
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PEGATRON PROPRIETARY AND CONFIDENTIAL

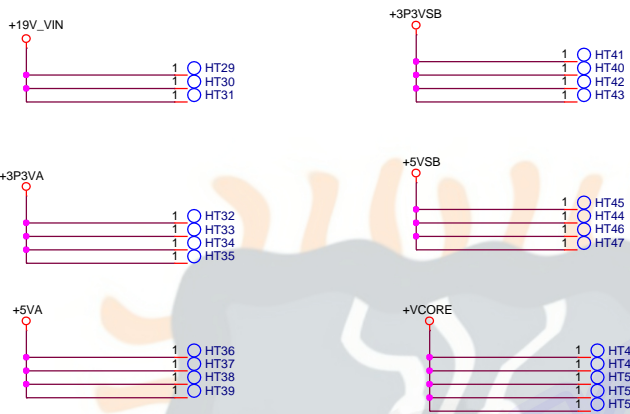
Pegatron Corp. Engineer: <Org>

Size: A3 Project Name: MFF

Date: Sheet 24

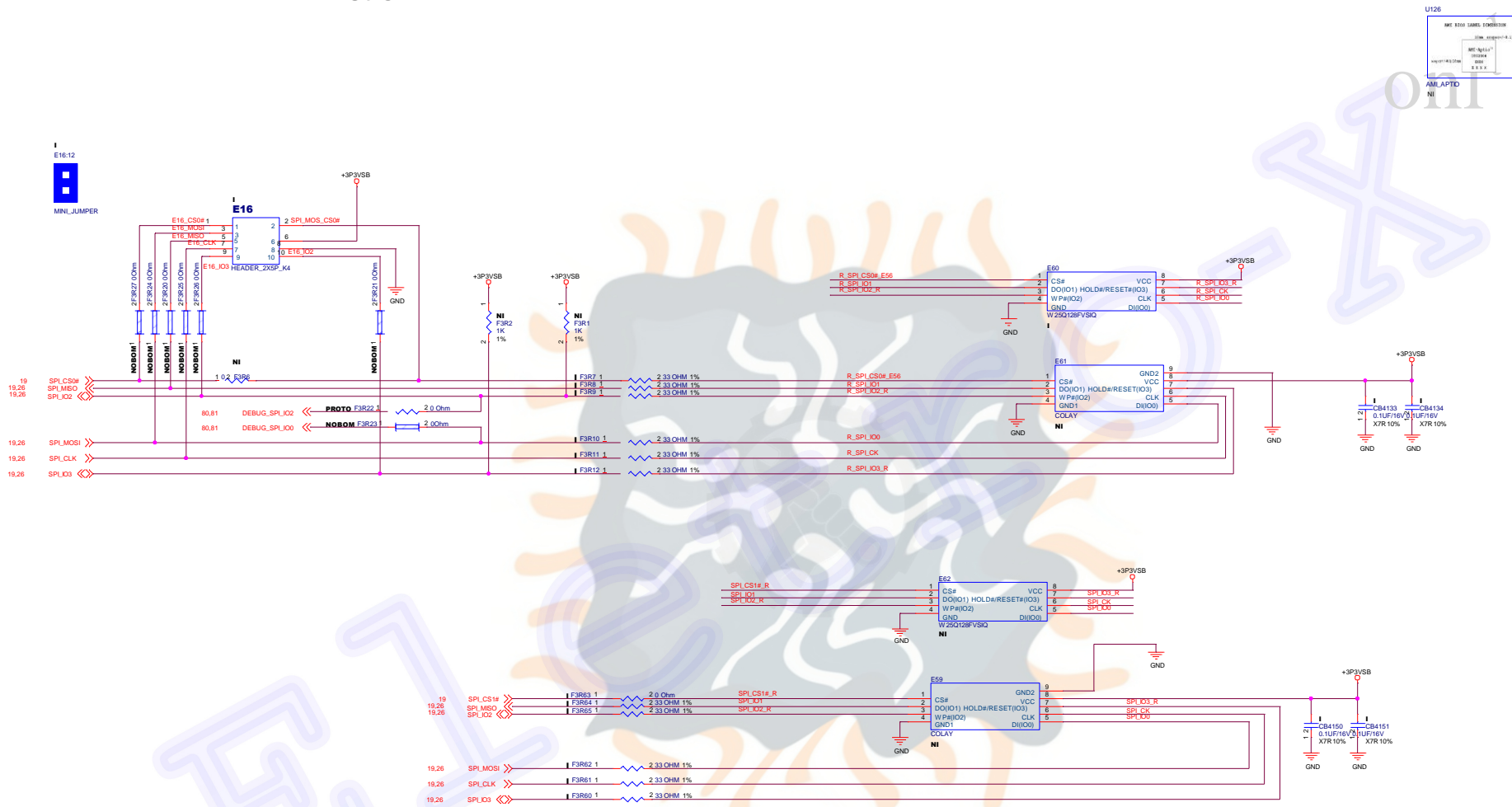


Eletro-X



Eletro-X

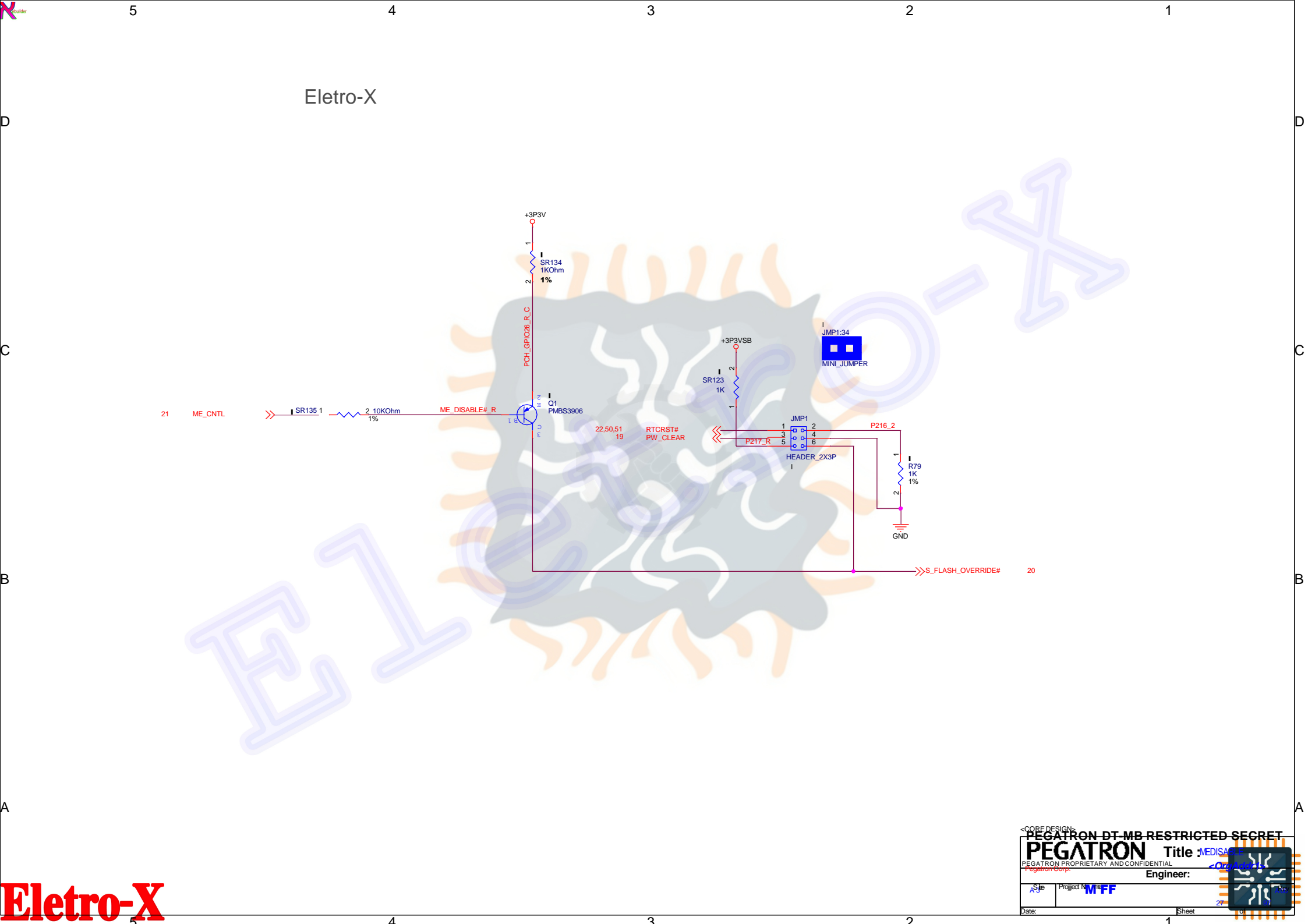
# Eletro-X



PEGATRON DT-MB RESTRICTED SECRET

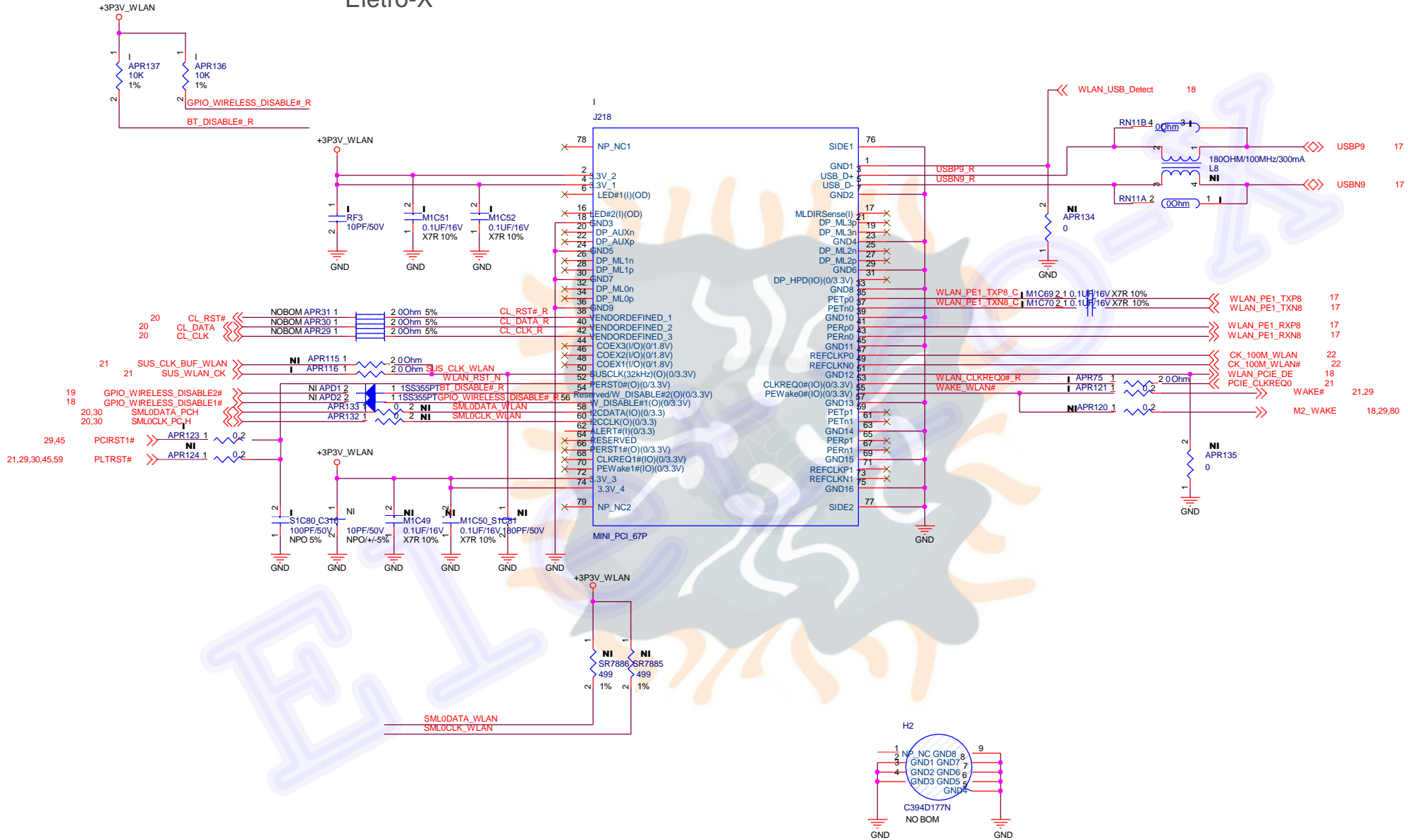
PEGATRON		Title: SPI
PEGATRON PROPRIETARY AND CONFIDENTIAL		
PEGATRON Corp.		Engineer: MFF
PEGATRON Name		Engineer
Date		Rev

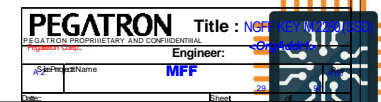
# Eletro-X



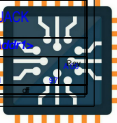
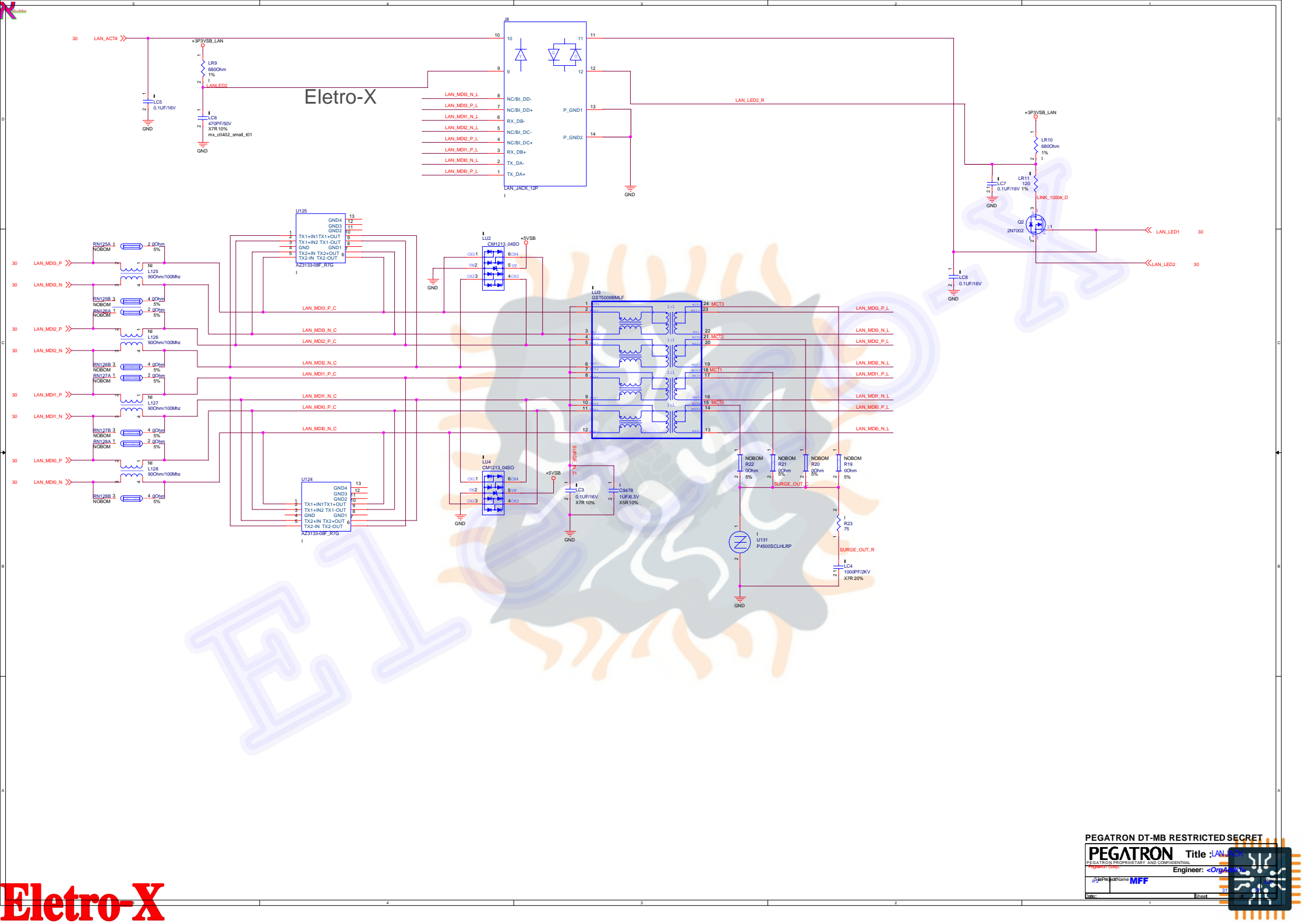
Eletro-X

# Eletro-X

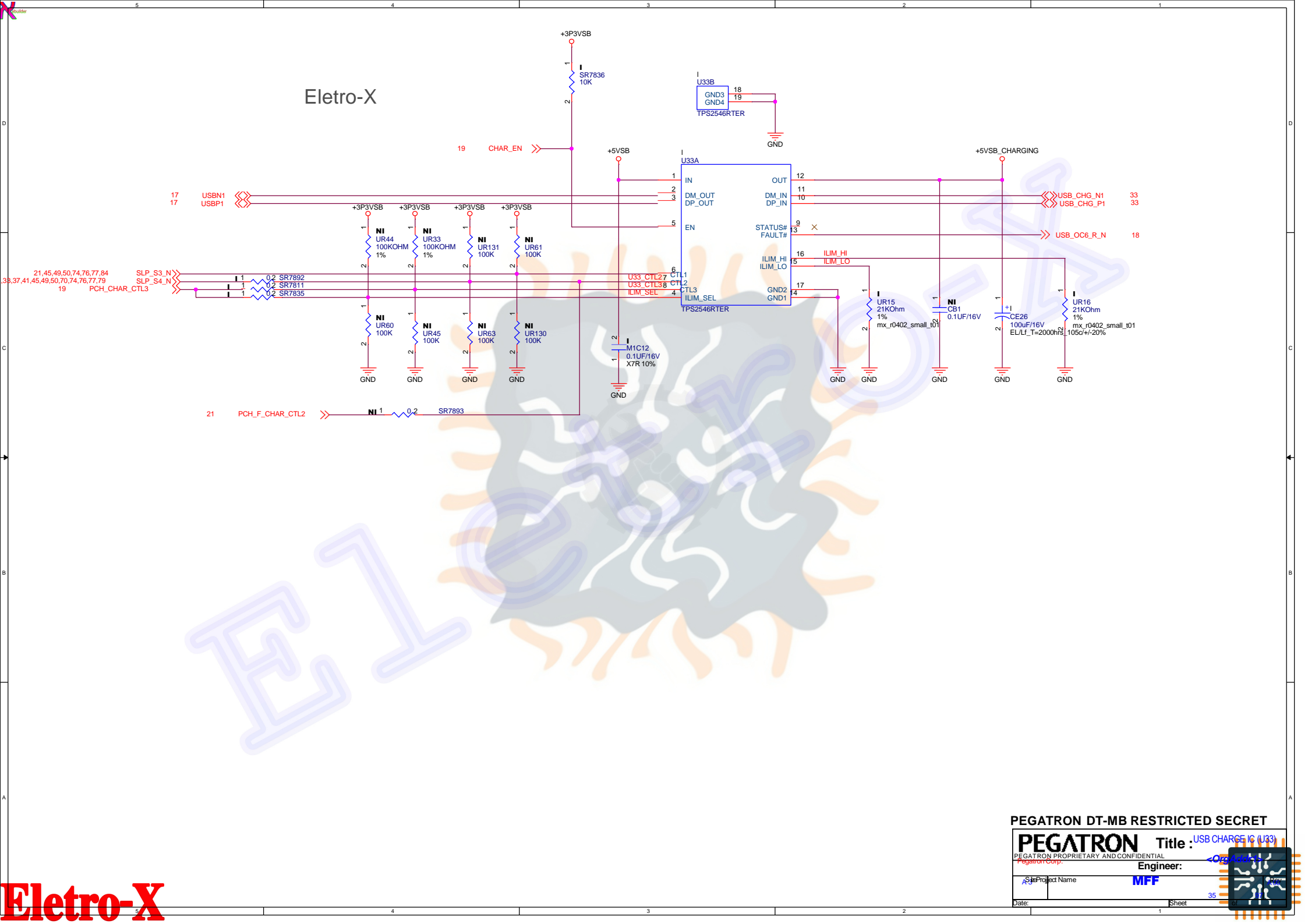










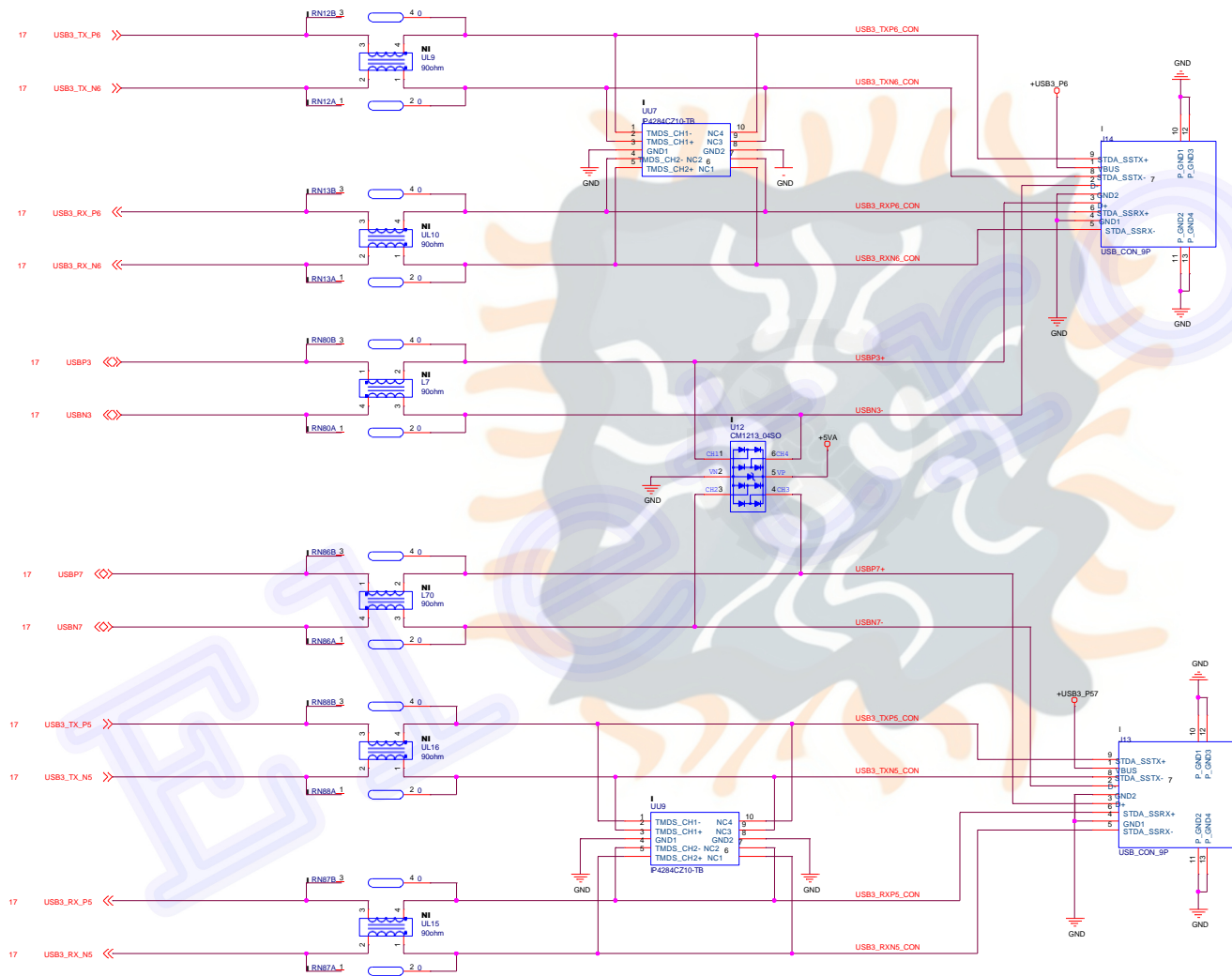
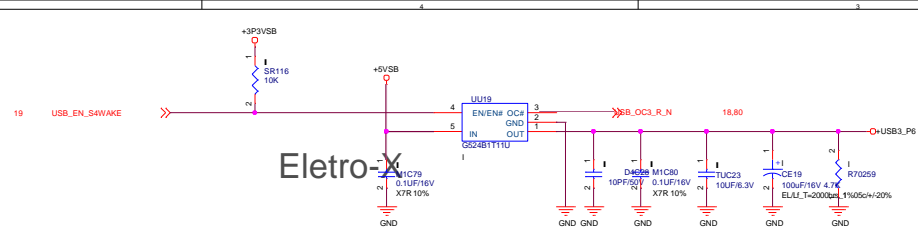


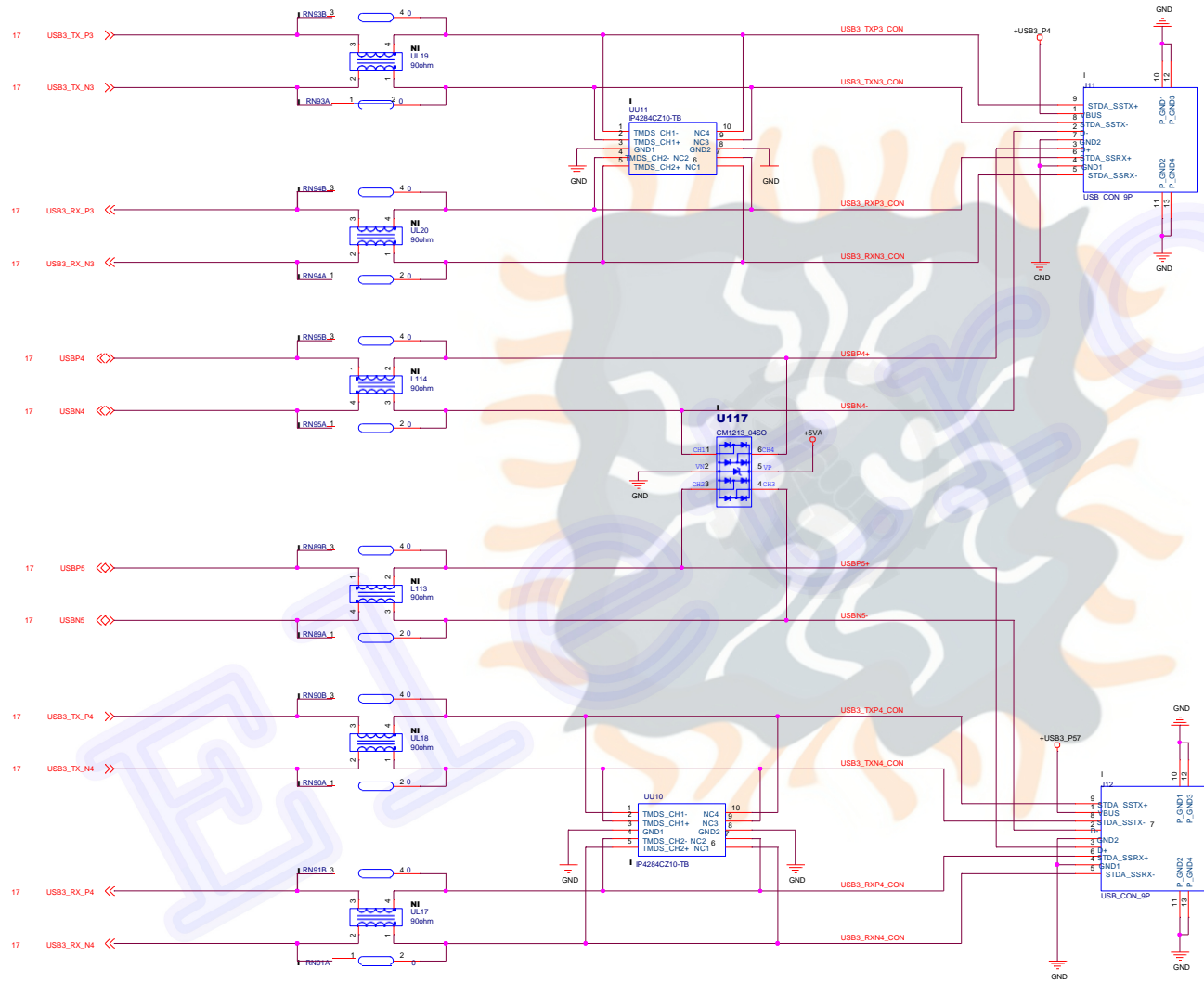
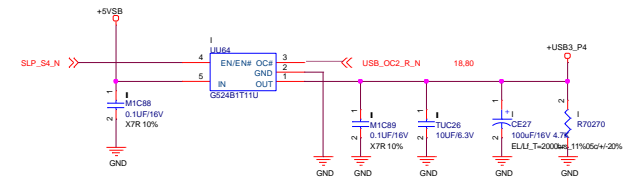
Eletro-X

PEGATRON DT-MB RESTRICTED SECRET

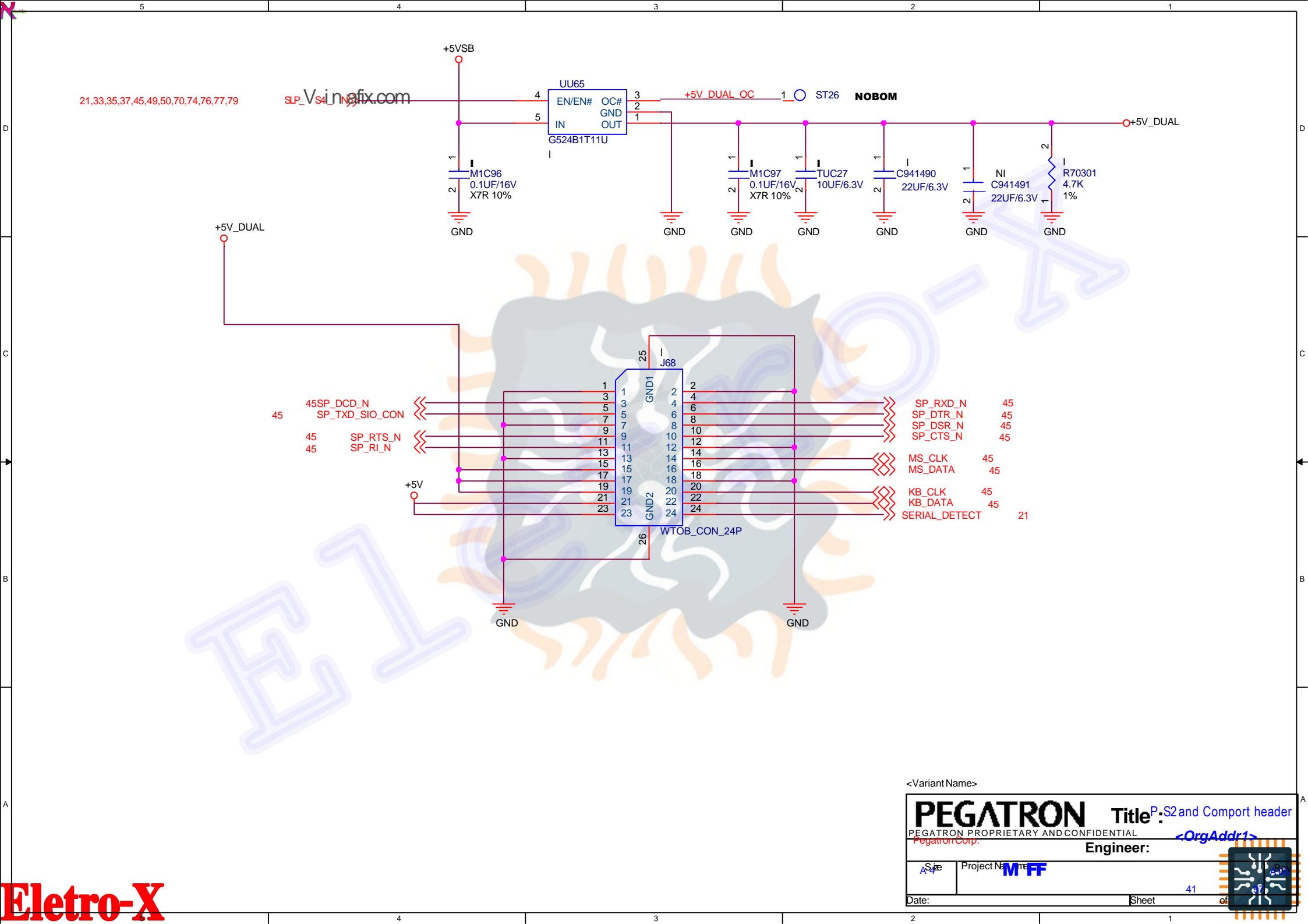
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PEGATRON PROPRIETARY AND CONFIDENTIAL		Engineer: MFF	
Project Name		Date:	
Sheet		35	







PEGATRON DT-MB RESTRICTED SECRET



21,33,35,37,45,49,50,70,74,76,77,79

Vinfix.com

45

45SP\_DCD\_N  
SP\_TXD\_SIO\_CON

45 SP\_RTS\_N  
45 SP\_RI\_N

+5V

GND

GND

<VariantName>

**PEGATRON** Title: P-S2 and Comport header  
PEGATRON PROPRIETARY AND CONFIDENTIAL <OrgAddr1>

Engineer:

Size

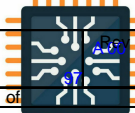
Project Name

MFF

Date:

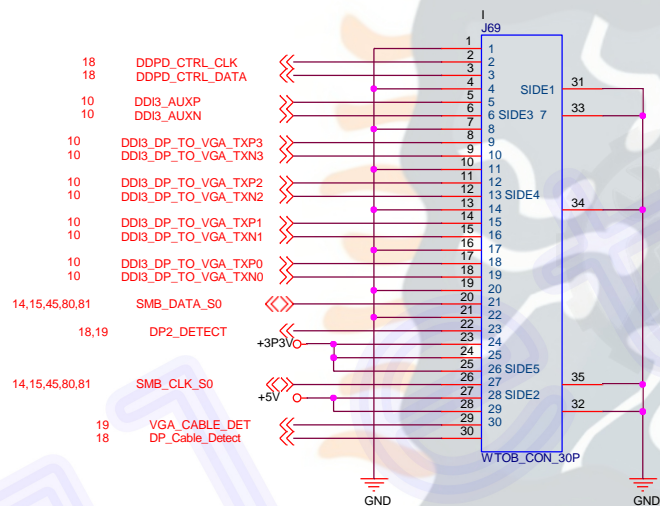
Sheet

41

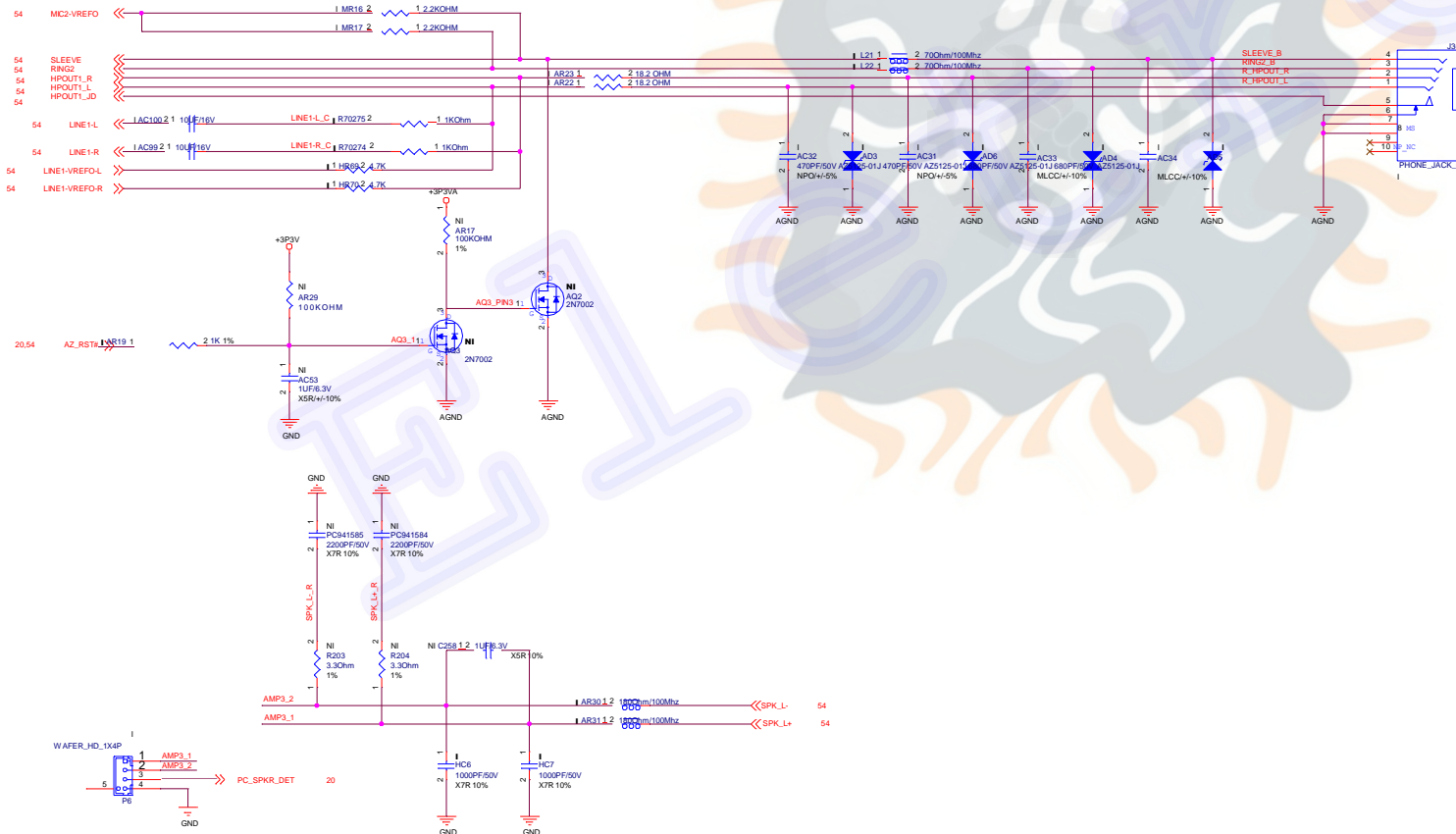
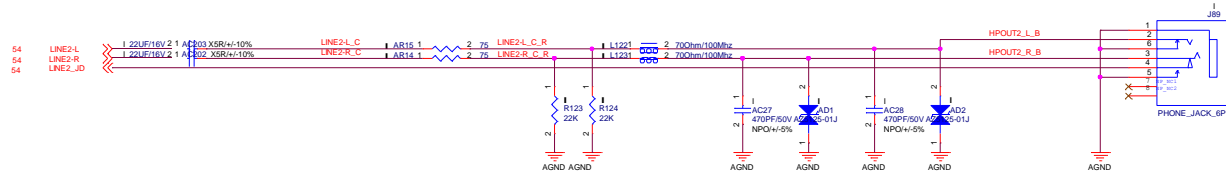


Eletro-X

Eletro-X



# Eletro-X



Eletro-X

## Eletro-X

D

D

C

C

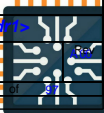
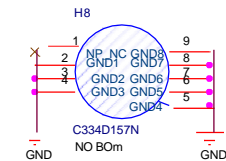
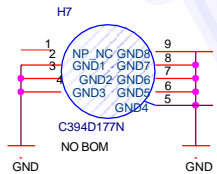
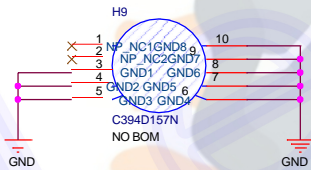
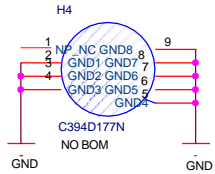
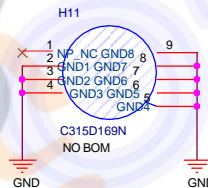
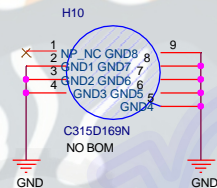
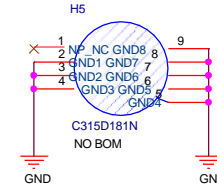
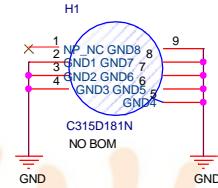
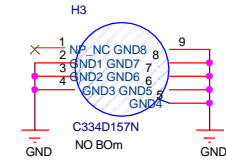
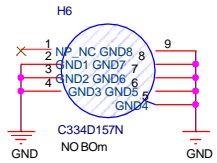
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B

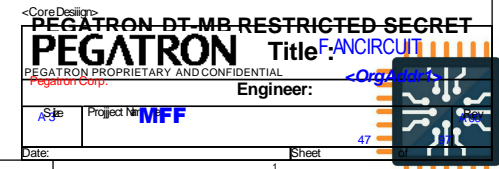
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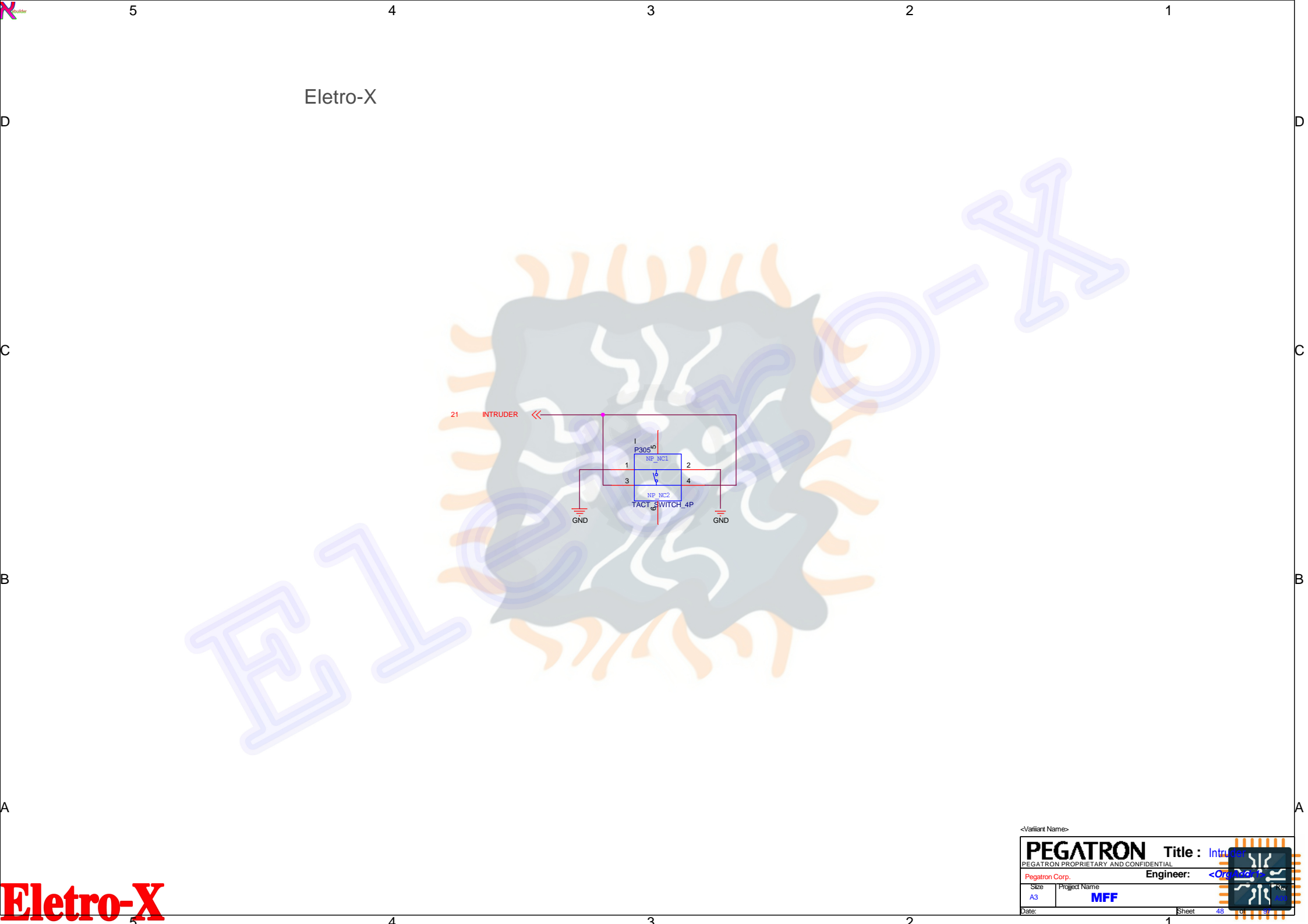
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Eletro-X

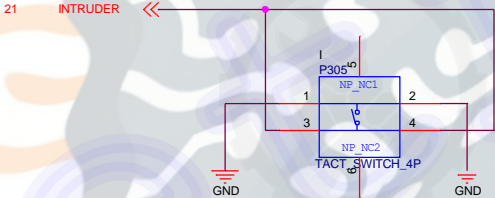


# Eletro-X






Eletro-X

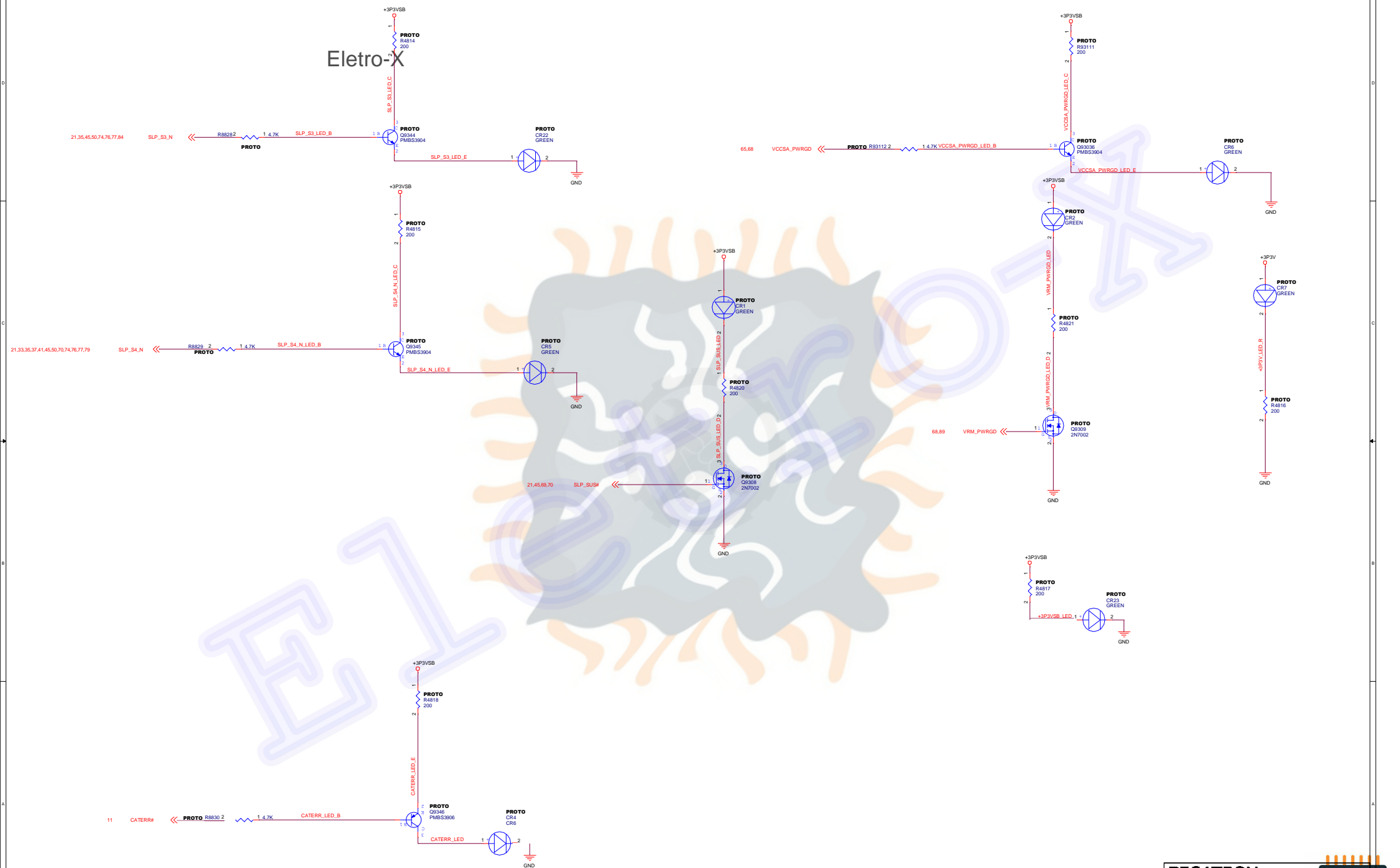


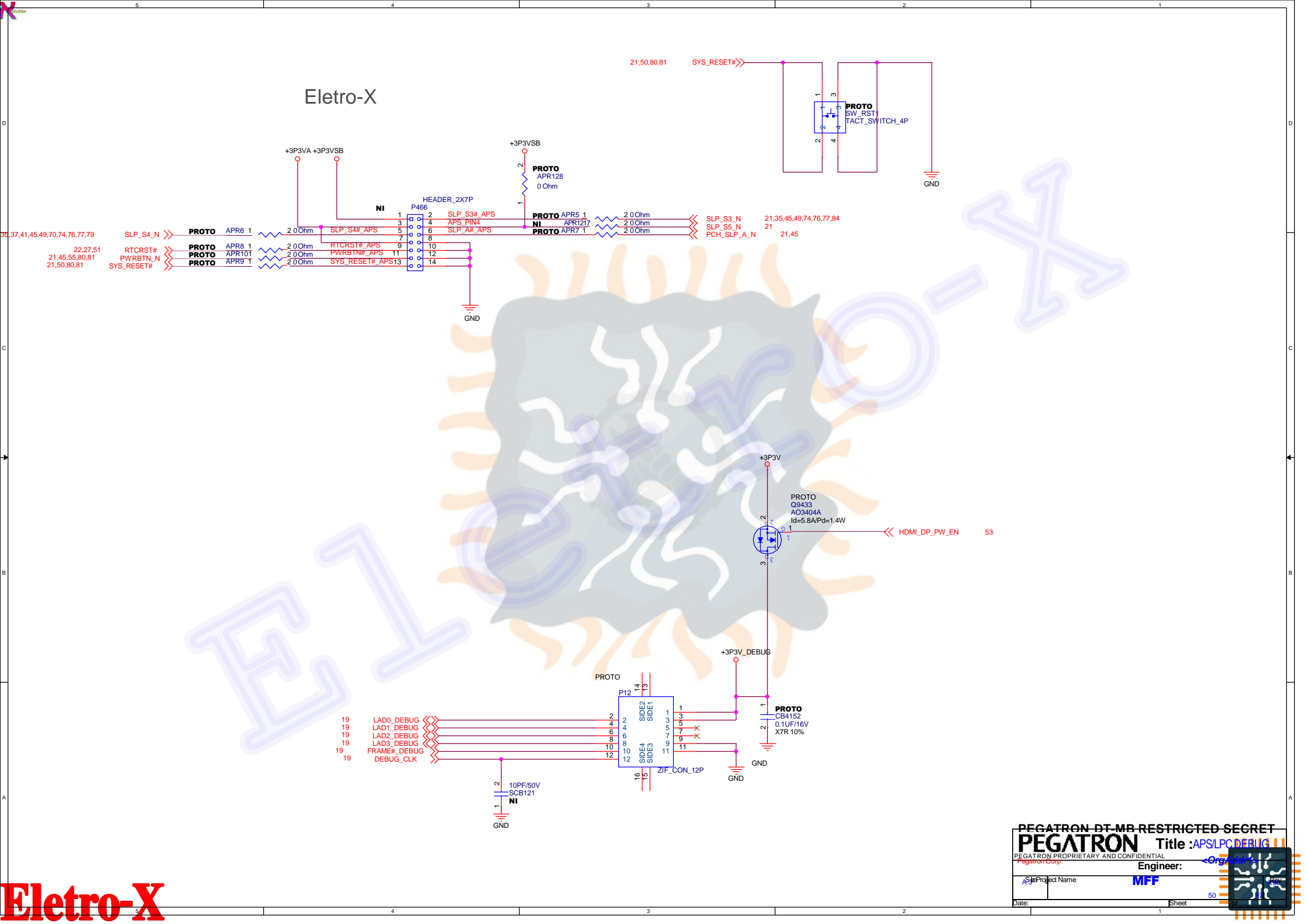
Eletro-X

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PEGATRON PROPRIETARY AND CONFIDENTIAL			
Pegatron Corp.		Engineer: <Original>	
Size	Project Name		
A3	MFF		
Date:	Sheet		48







Eletro-X

PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title :APS/LPC DEBUG

PEGATRON PROPRIETARY AND CONFIDENTIAL

Engineer: MFF

Project Name

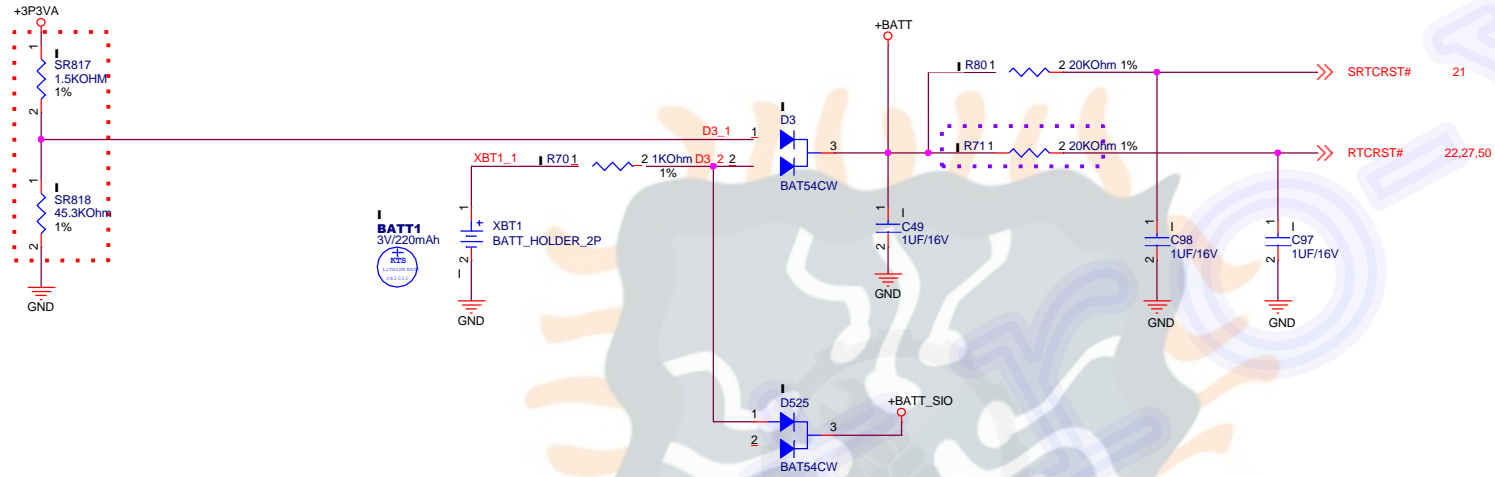
Date: Sheet 50

Eletro-X

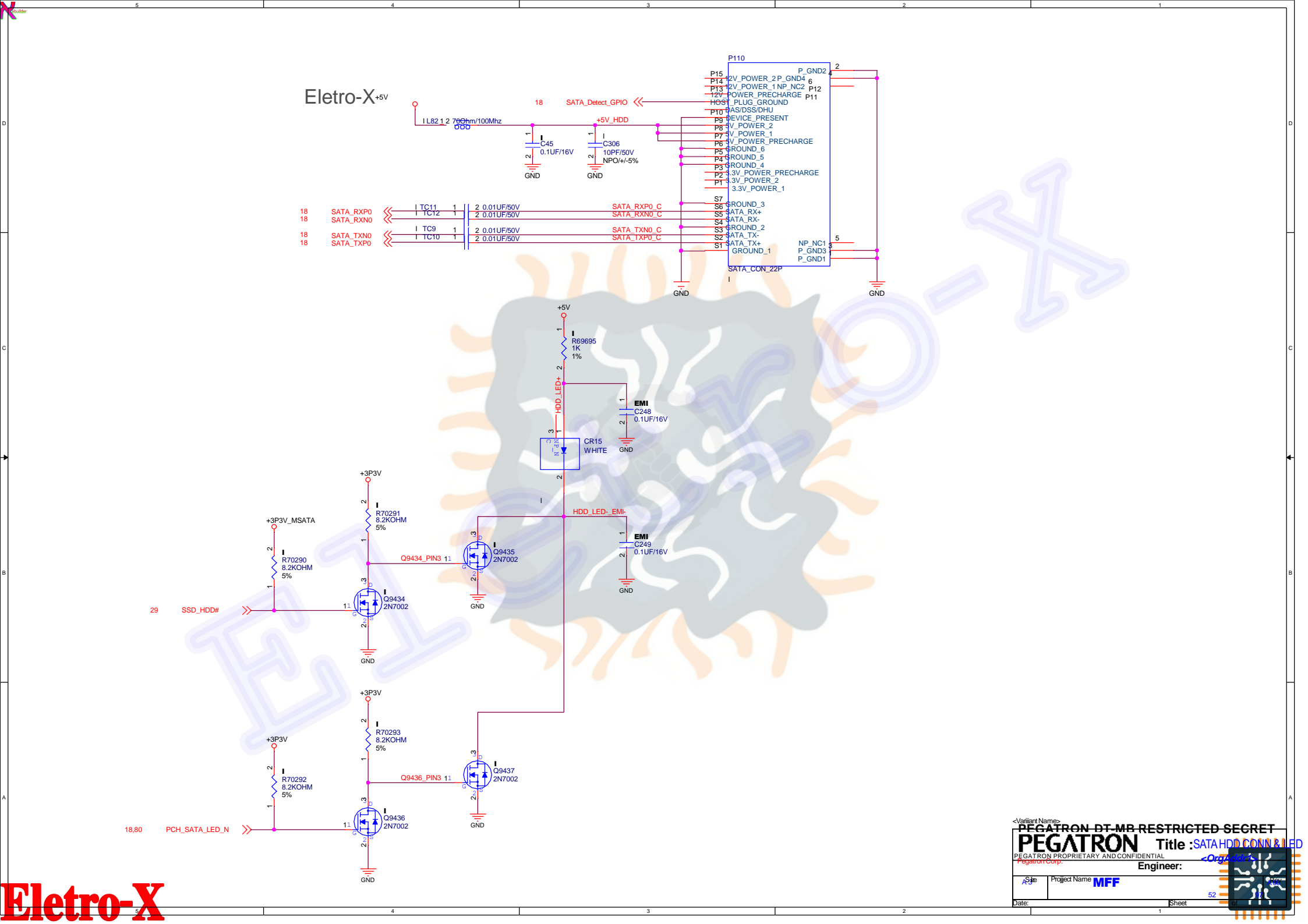
RTC

1103 by PDG p324  
VCCDSW 3p3 to drop the voltage and meet the max  
VCCRTC < 3.2V specification. The cost impact is small but  
this solution will result in an ~0.25 mW power loss in  
non-G3 state. In this solution, the dual diodes must be in a  
single SI package to ensure matched Vf characteristics.  
Initial resistor value recommendations are R2=1.5 KΩ and  
R3=45 KΩ.

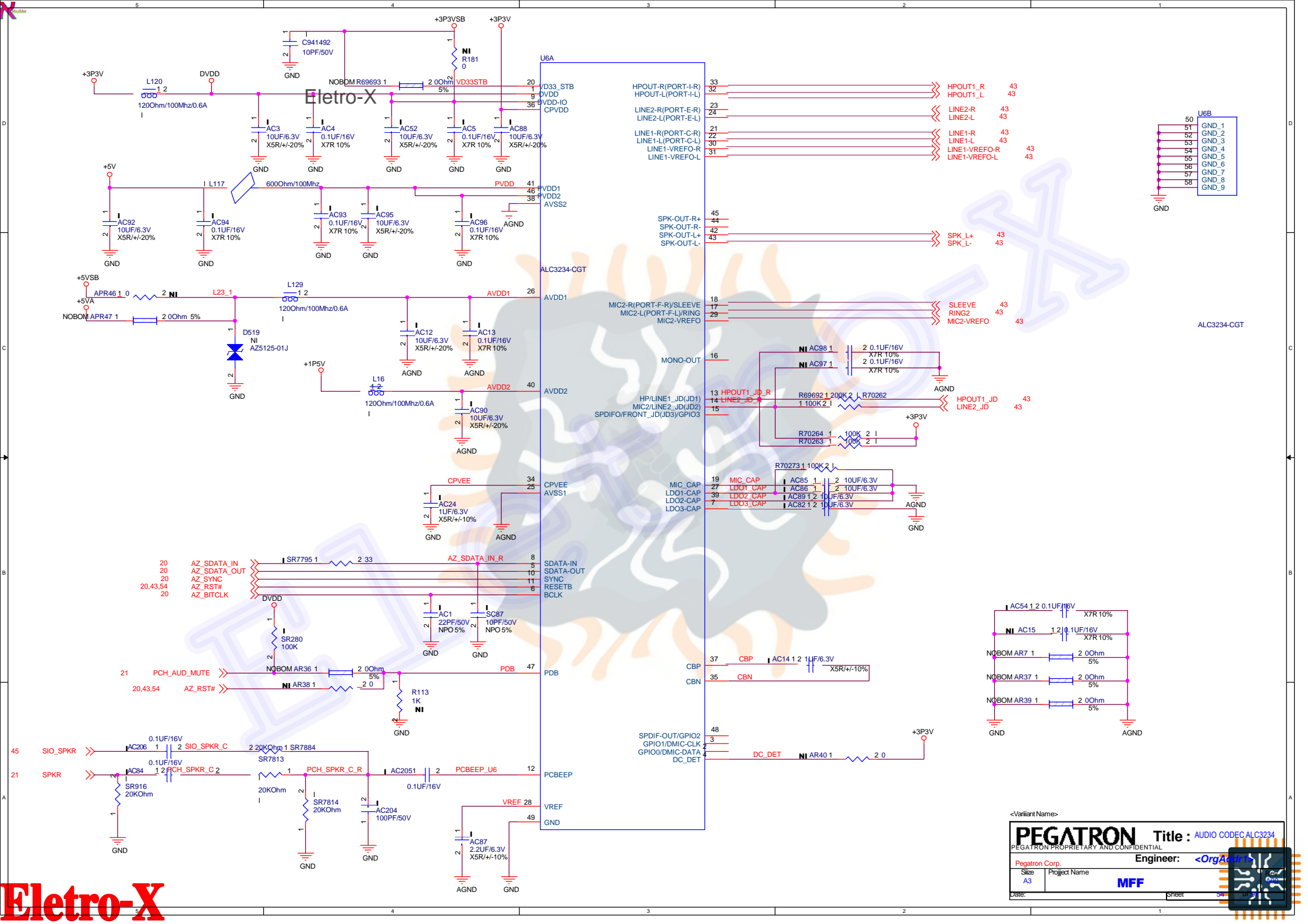
Original circuit design 27.4K(10X212274310)  
Intel Sequence asked 20K 1%, CRB asked 30.1K.



Eletro-X







Eleto-X

<Variant Name>

PEGATRON

PEGATRON PROPRIETARY AND CONFIDENTIAL

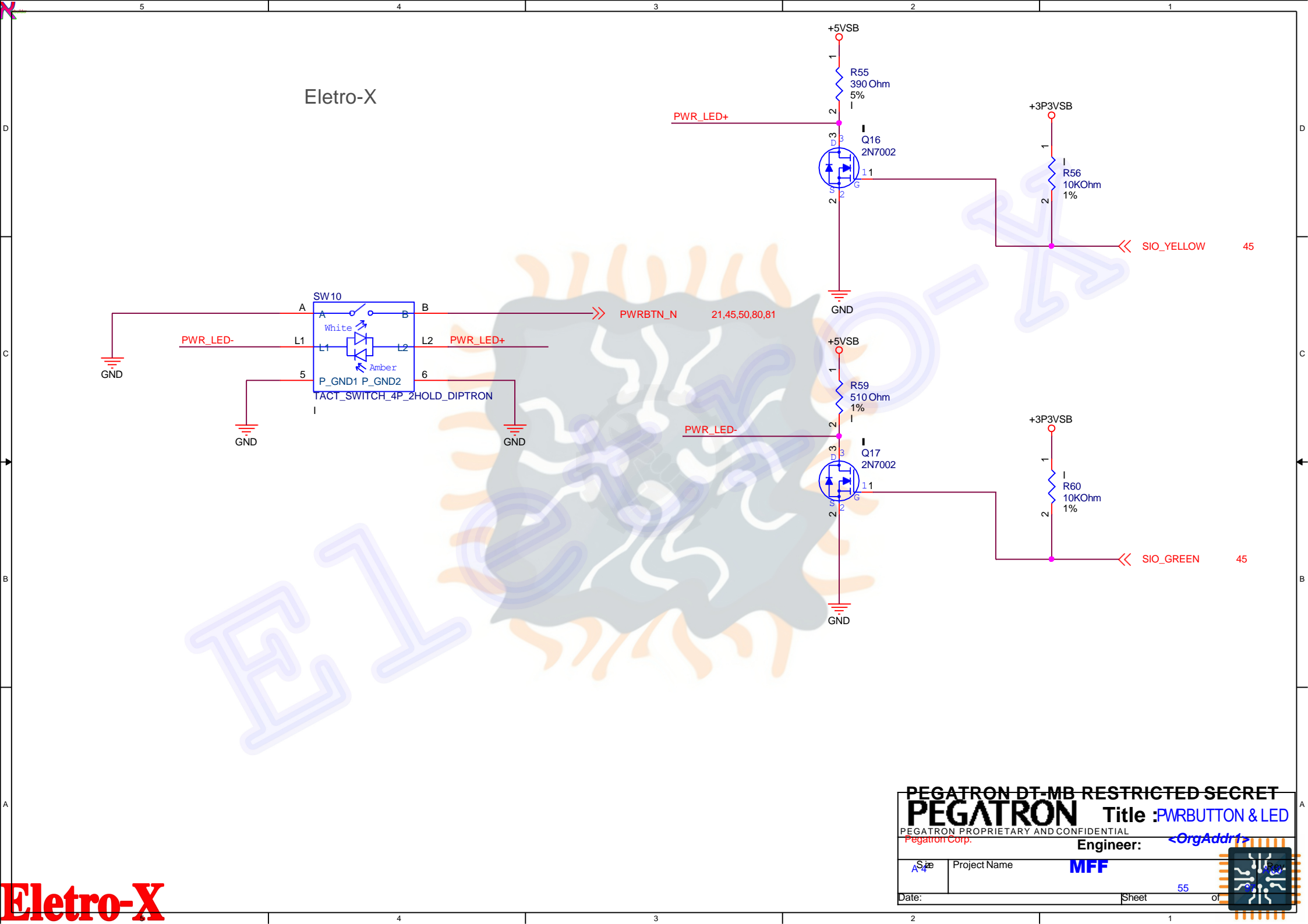
Engineer: <OrgAdmin>

Size  
A3

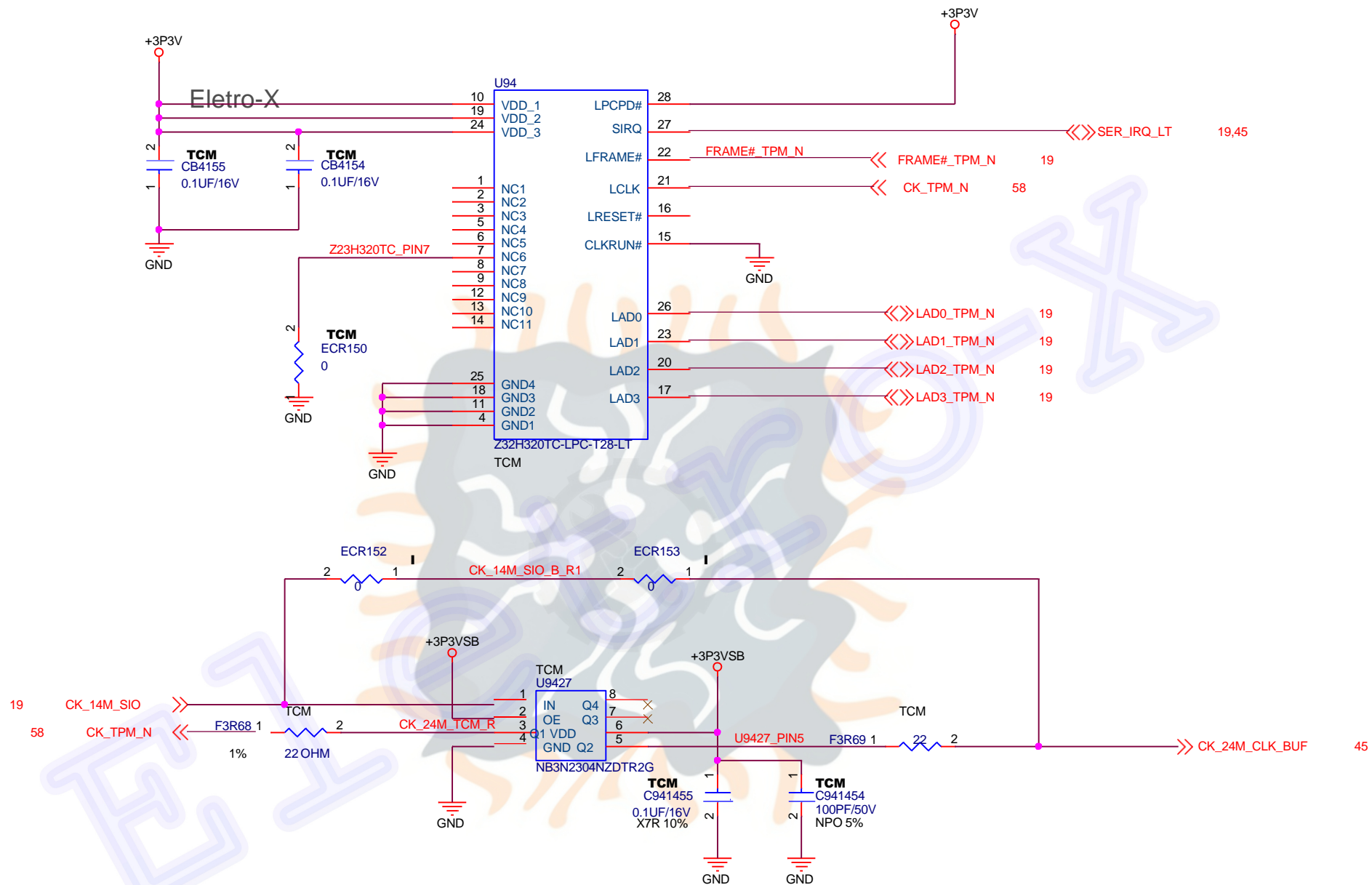
Project Name  
MFF

Title : AUDIO CODEC ALC3234

Date: \_\_\_\_\_ Sheet 54 of 59



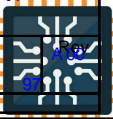
Eletro-X

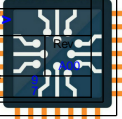
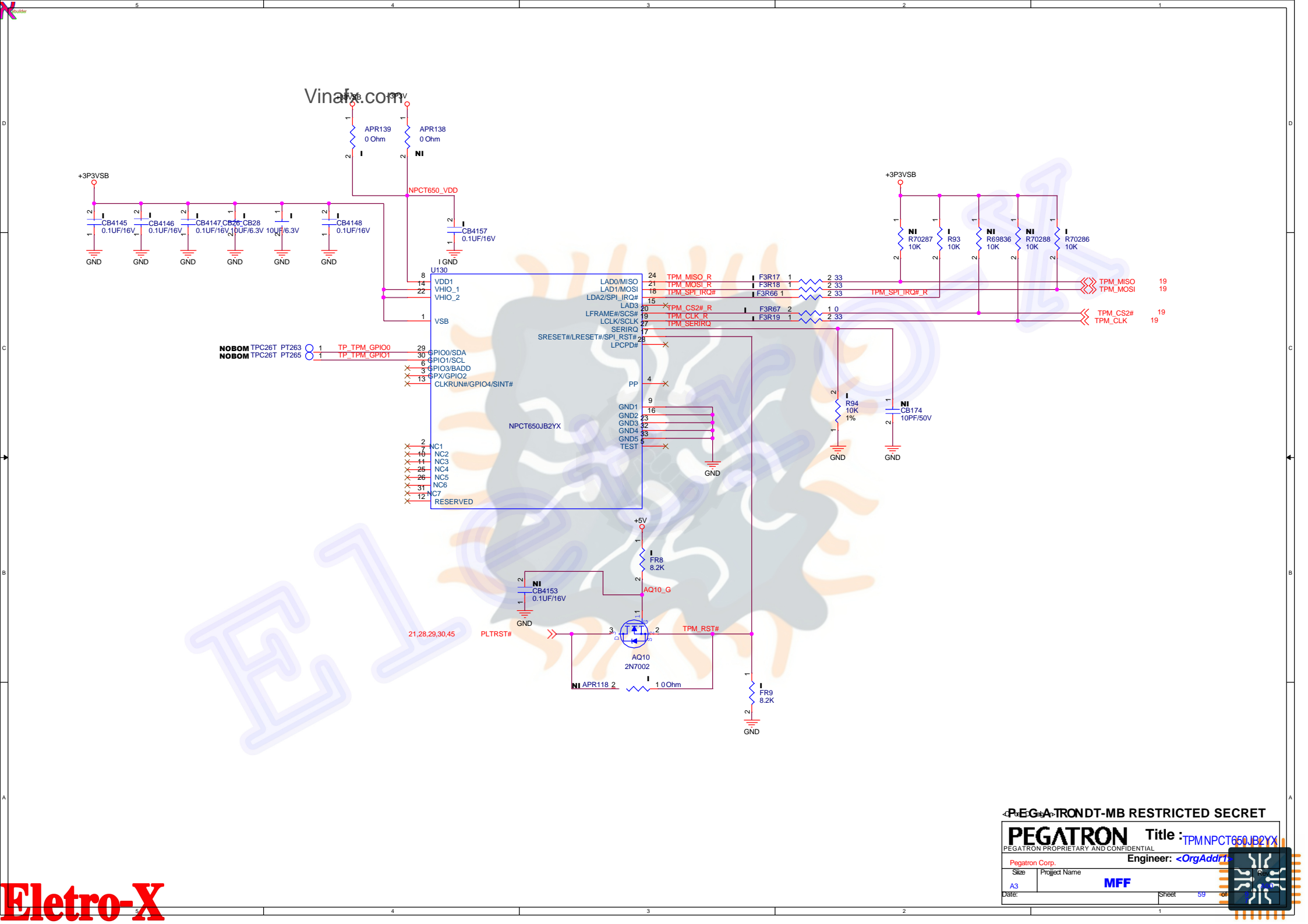


PEGATRON DT-MB RESTRICTED SECRET

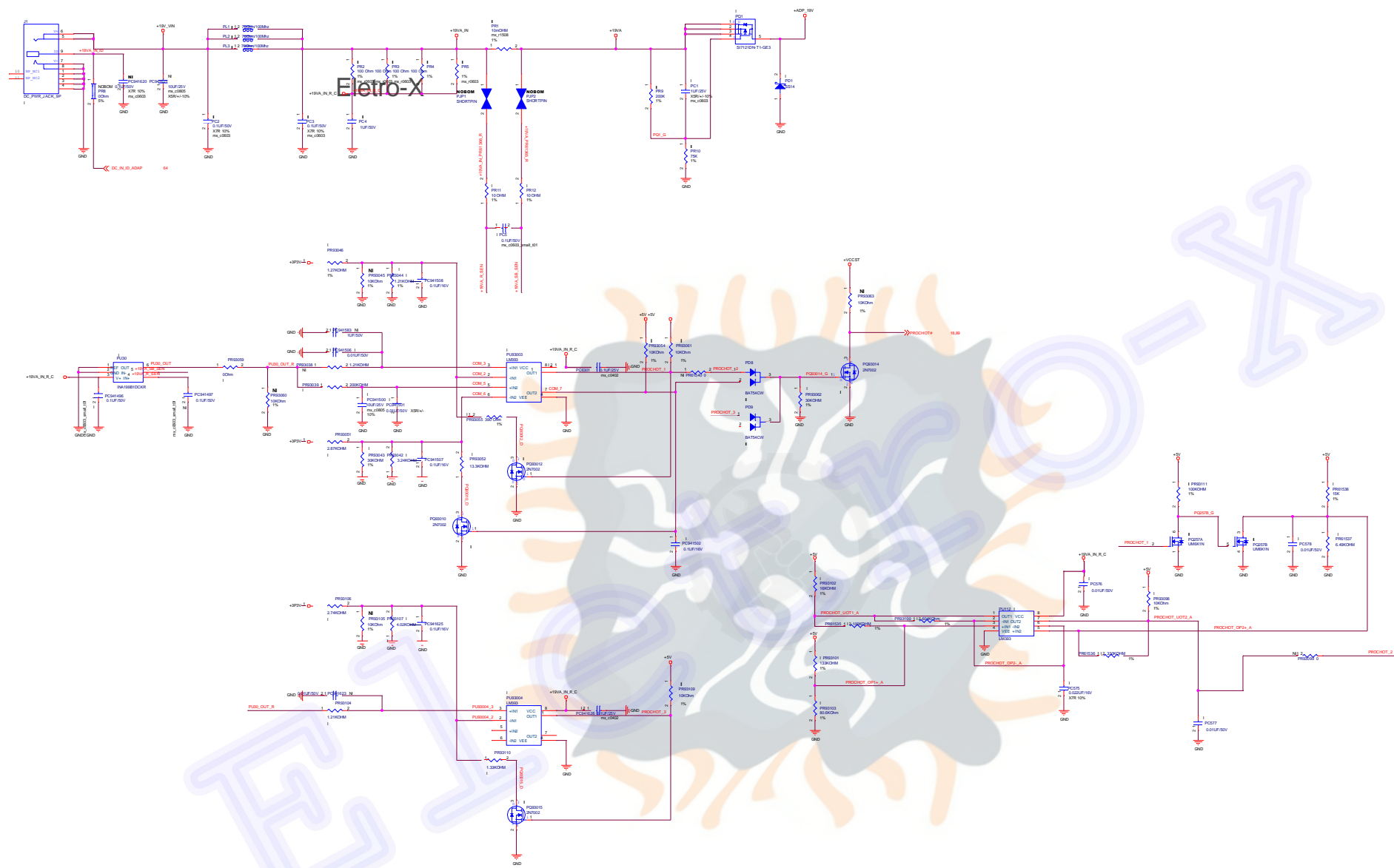
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PEGATRON PROPRIETARY AND CONFIDENTIAL			
Pegatron Corp.		Engineer: <OrgAddr1>	
Size	Project Name		
A4	MFF		
Date:	Sheet 58 of		

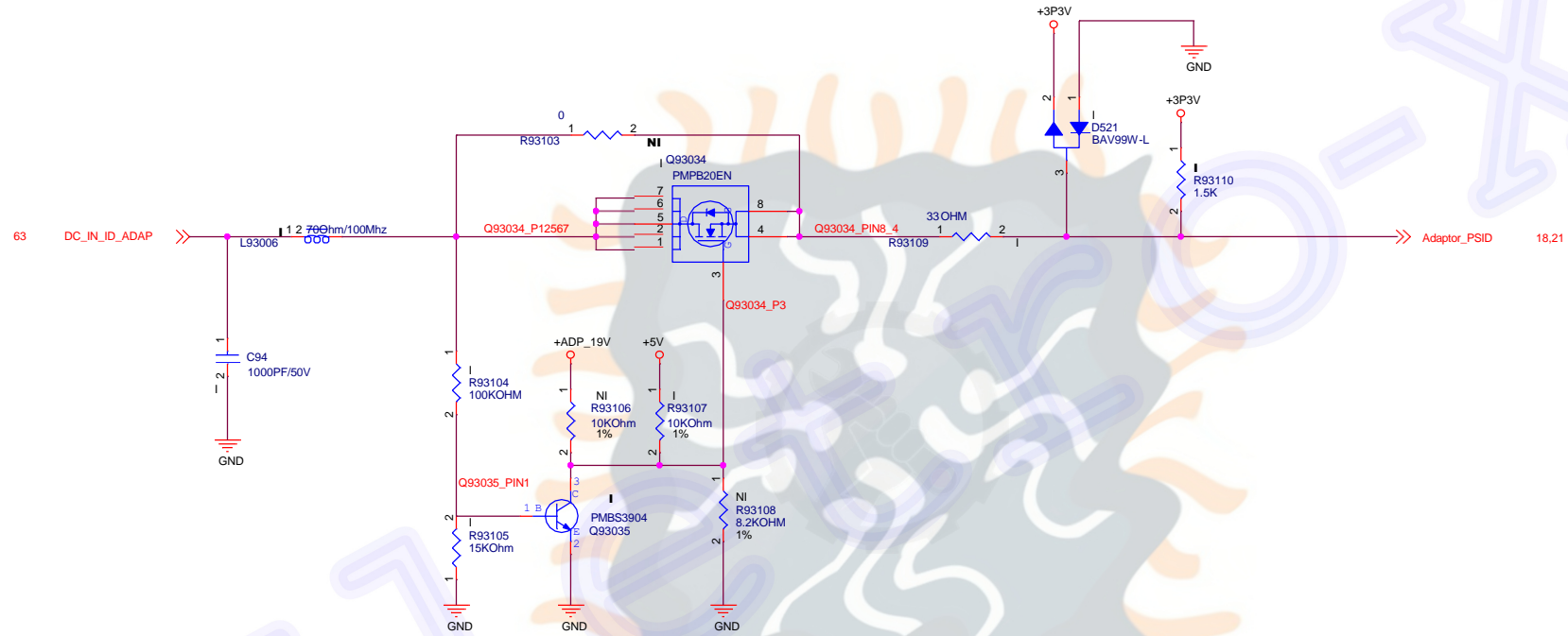






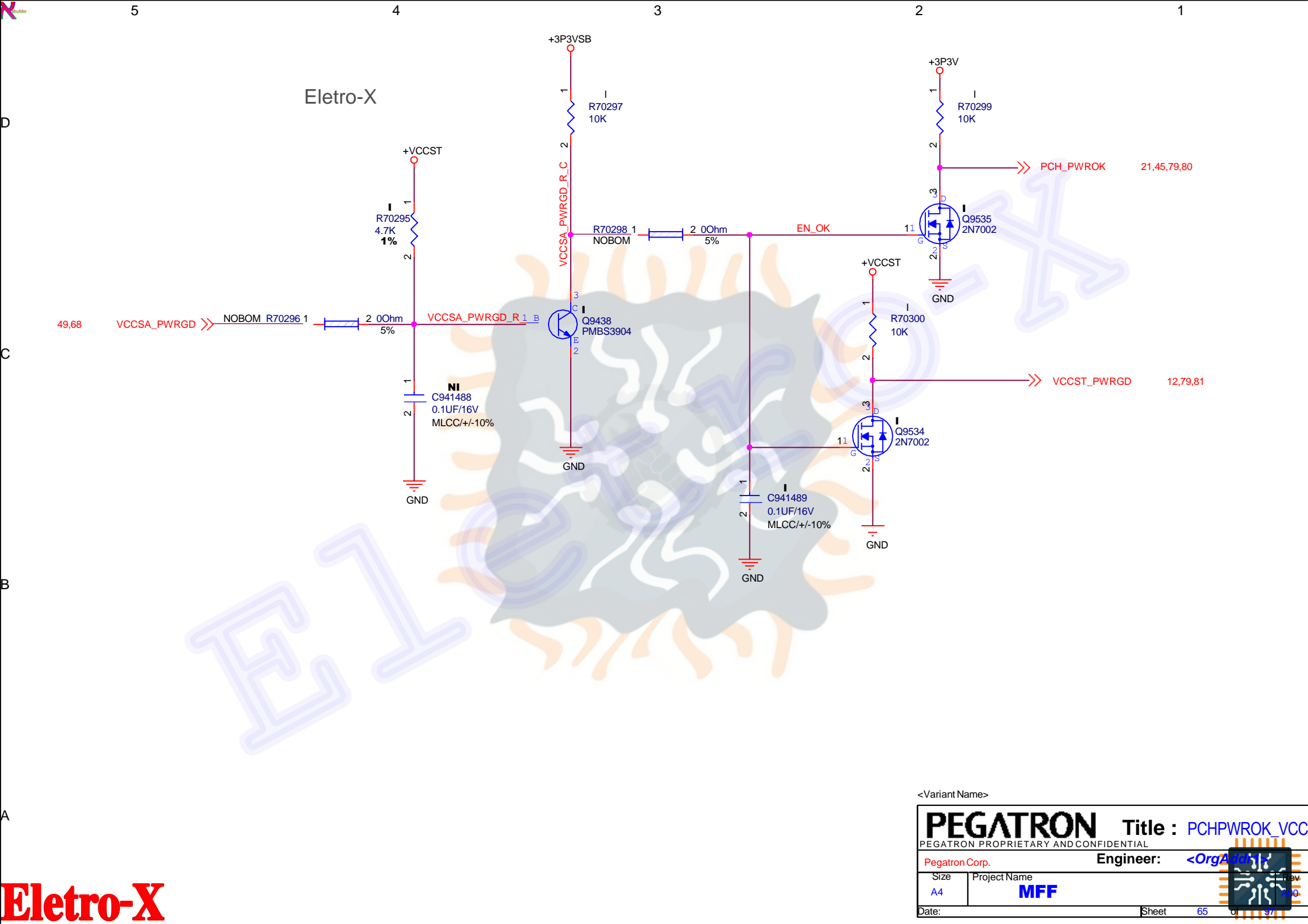


# Eletro-X



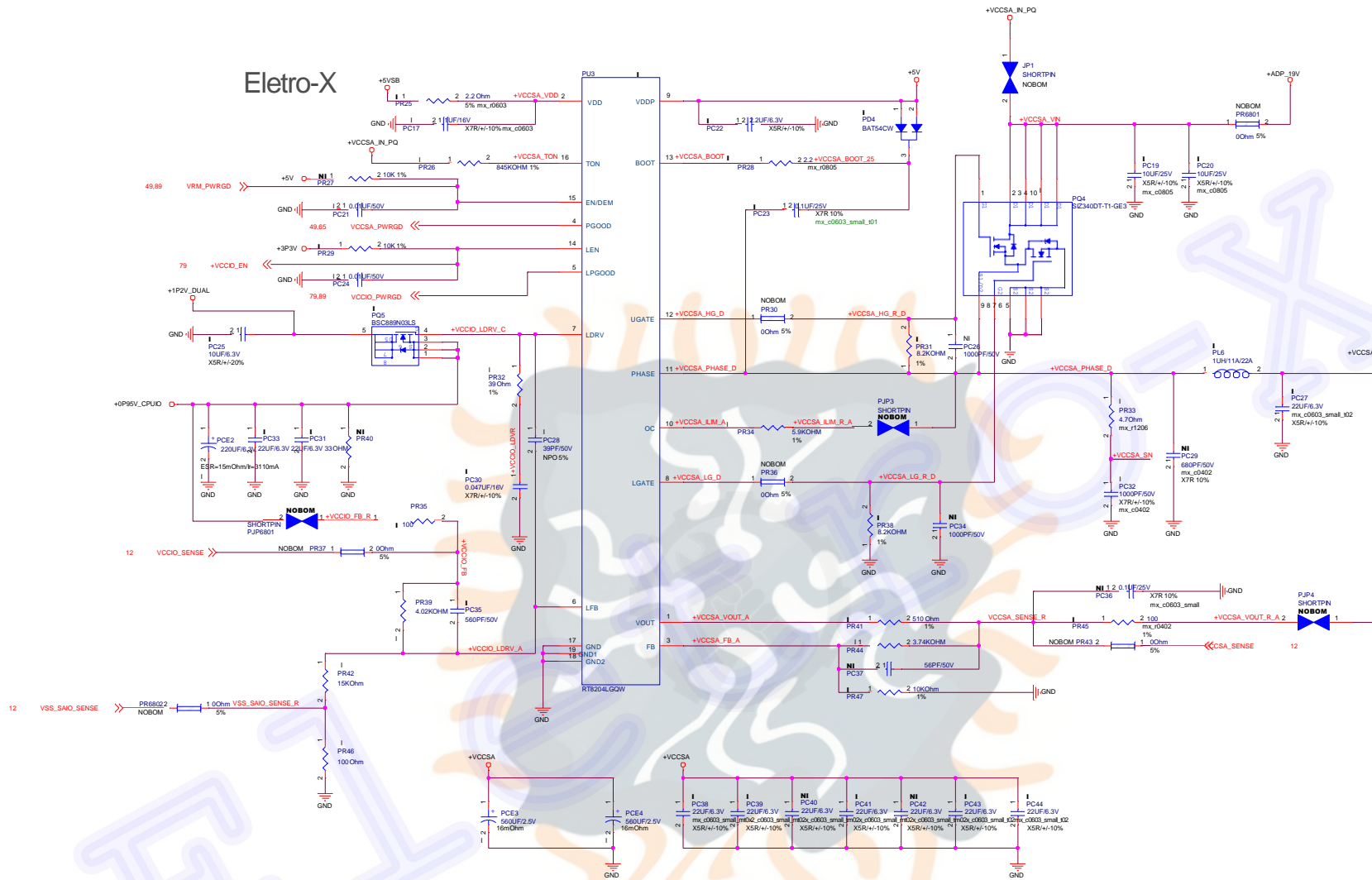
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PEGATRON PROPRIETARY AND CONFIDENTIAL			
Pegatron Corp.		Engineer: <Org/Name>	
Size	Project Name		
A3	MFF		
Date:	Sheet 64		



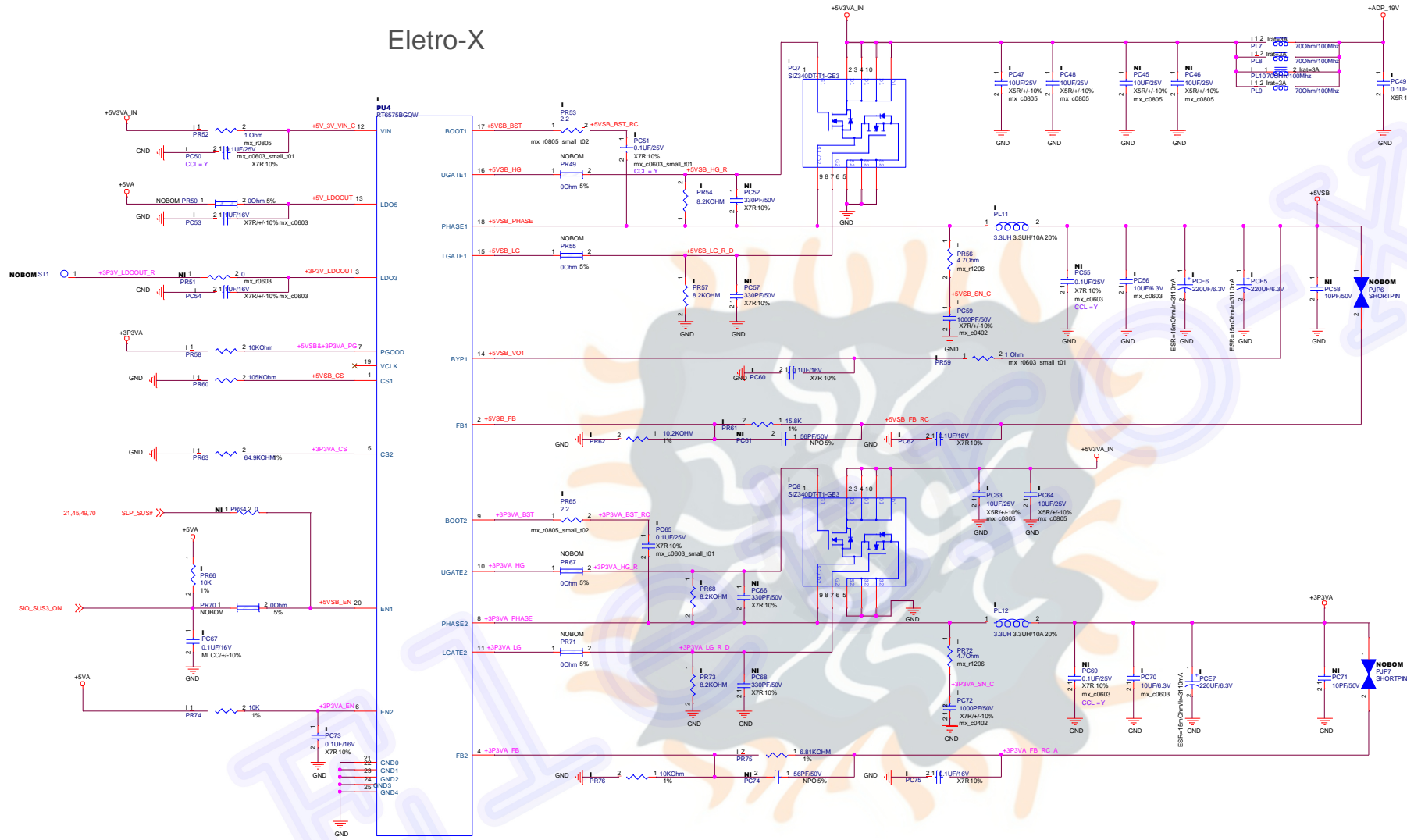
Eletro-X

# Eleto-X

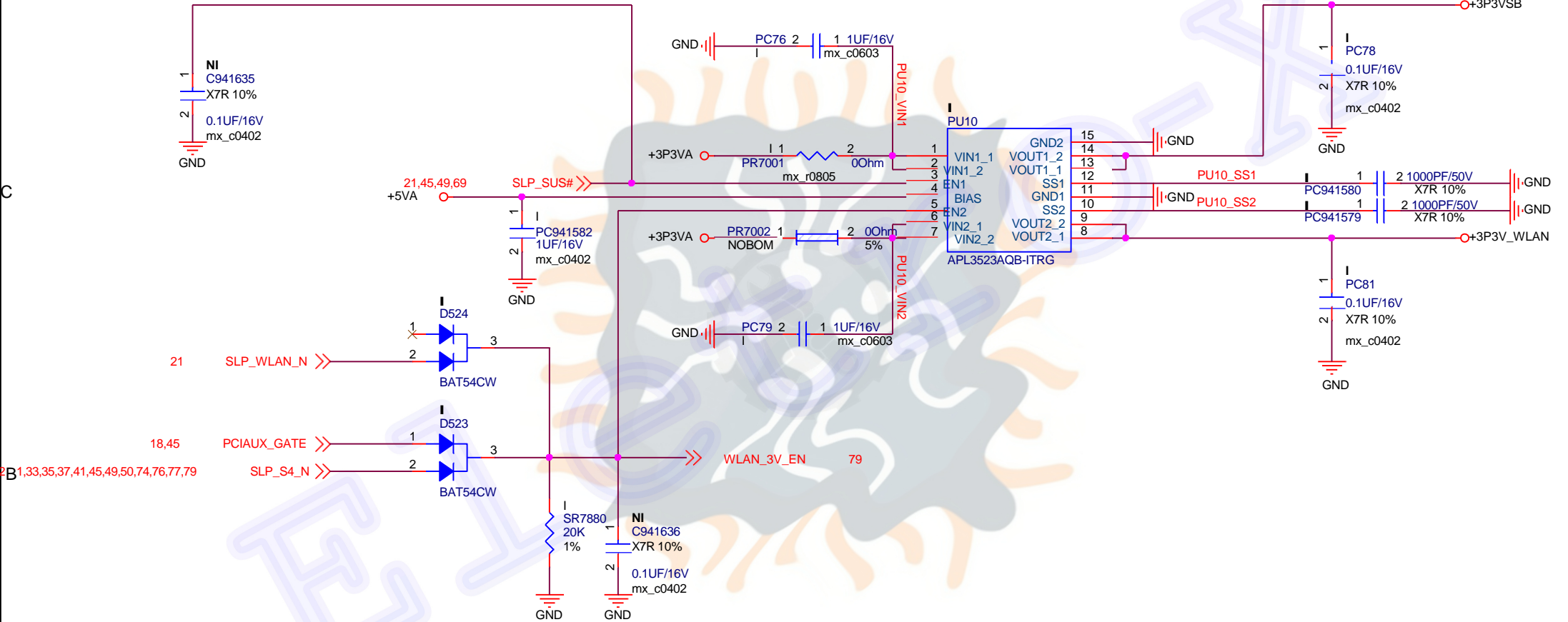


<VariantName>

# Eleto-X



## Eletro-X

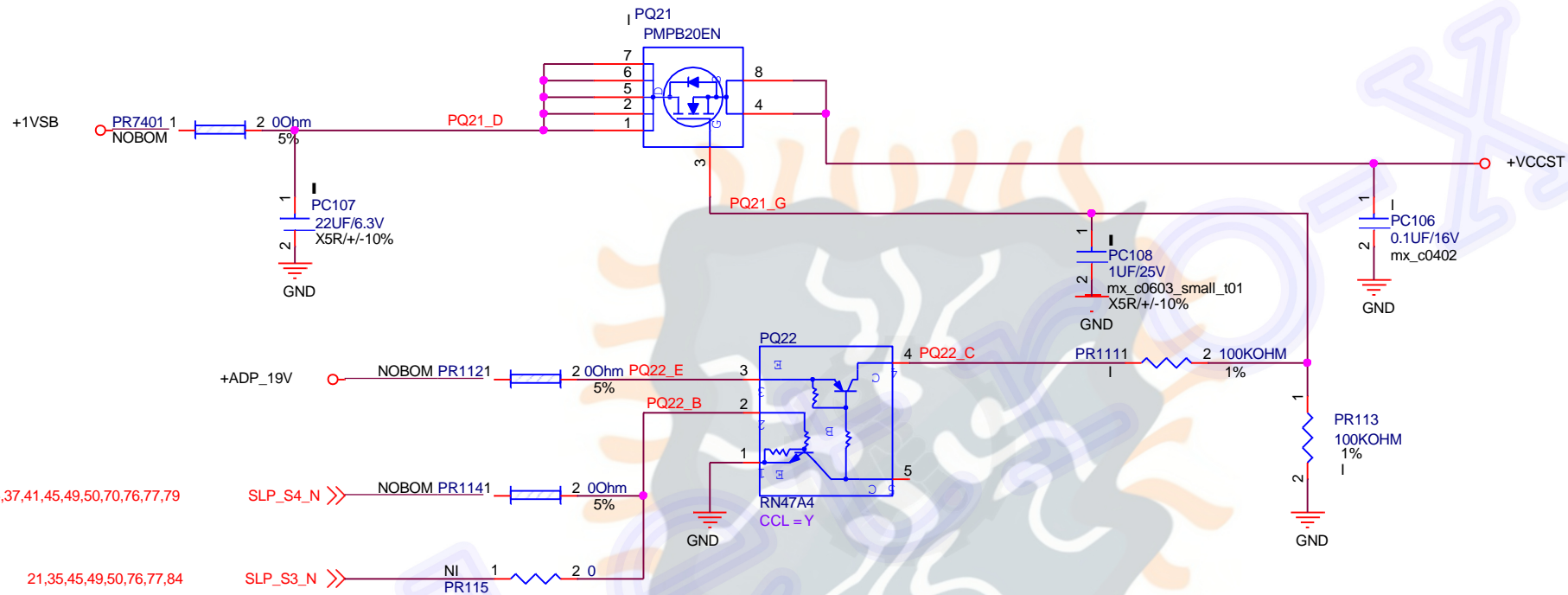


&lt;Variant Name&gt;

<b>PEGATRON</b>		Title : +3P3VSB/WLAN	
PEGATRON PROPRIETARY AND CONFIDENTIAL			
Pegatron Corp.		Engineer: <OrgAdmin>	
Size A4	Project Name <b>MFF</b>		
Date:	Sheet		70 of 87



## Eletro-X



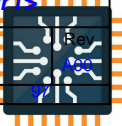
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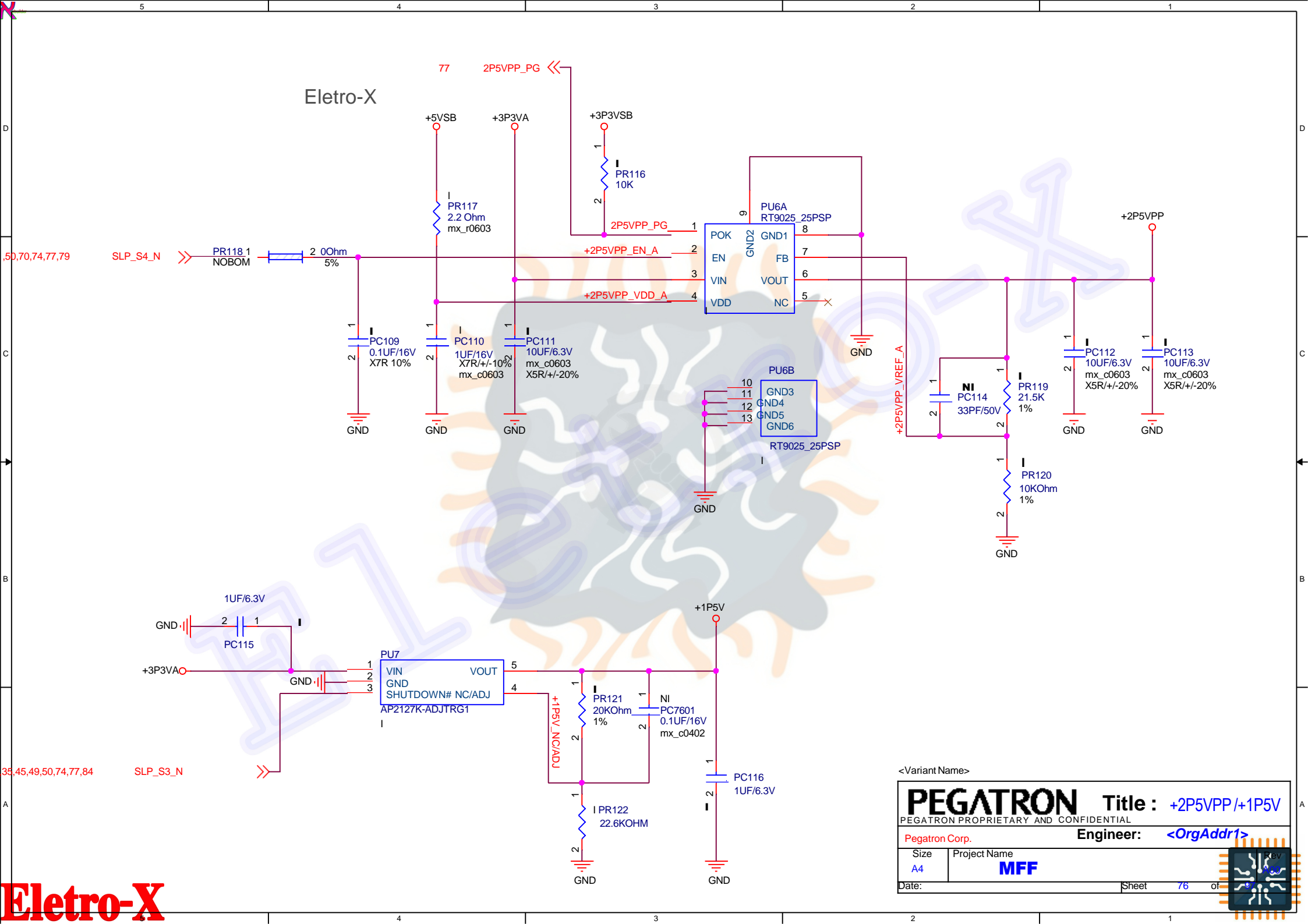
**PEGATRON** Title : +VCCST  
PEGATRON PROPRIETARY AND CONFIDENTIAL

Pegatron Corp. Engineer: &lt;OrgAddress&gt;

Size	Project Name
A4	MFF

Date: Sheet 74 of





Eletro-X

<Variant Name>

**PEGATRON** Title : +2P5VPP/+1P5V  
PEGATRON PROPRIETARY AND CONFIDENTIAL

Pegatron Corp.

Engineer: <OrgAddr1>

Size  
A4

Project Name  
MFF

Date:

Sheet

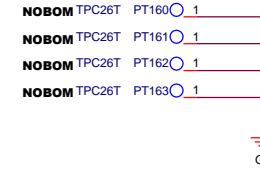
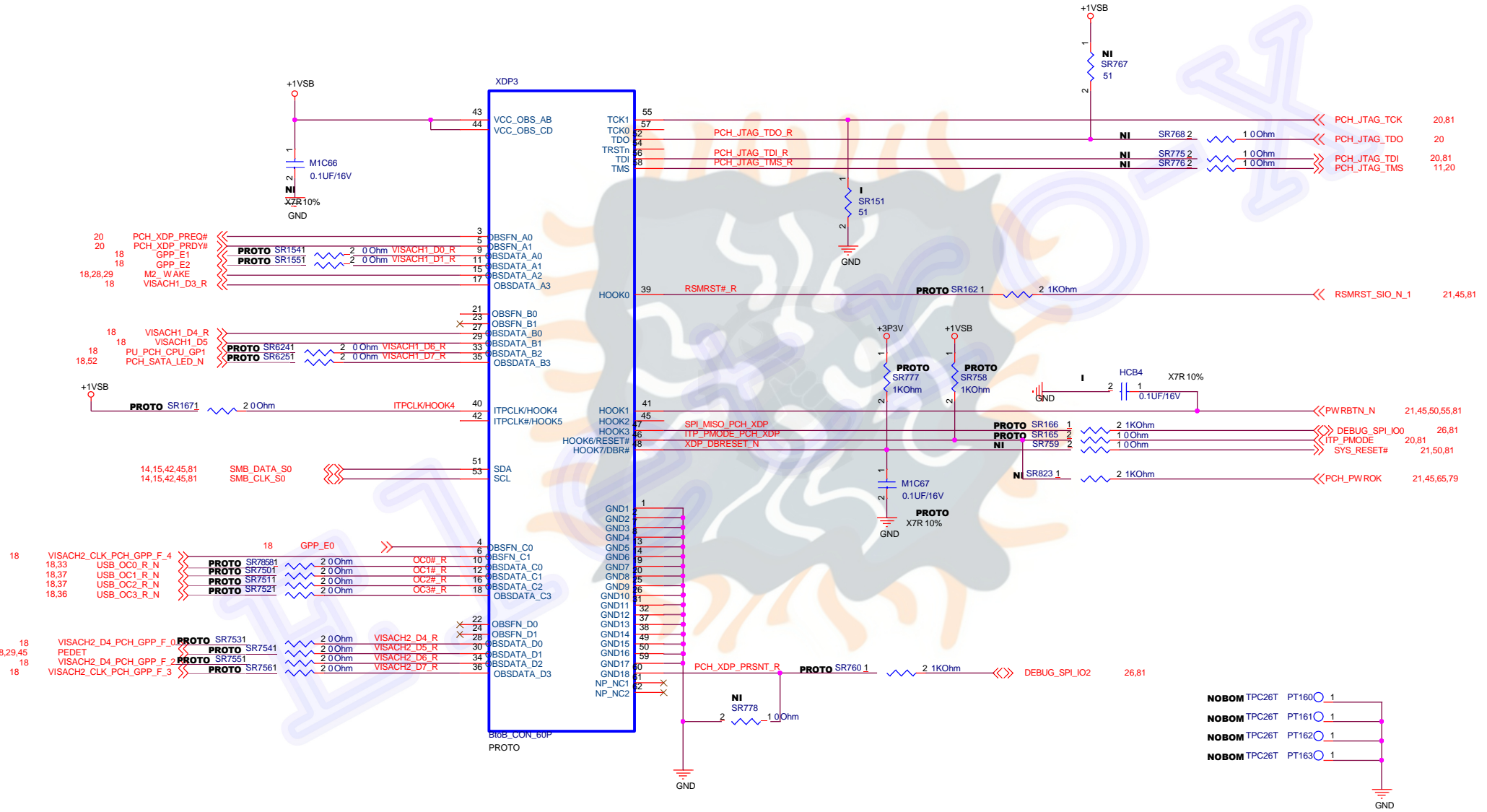
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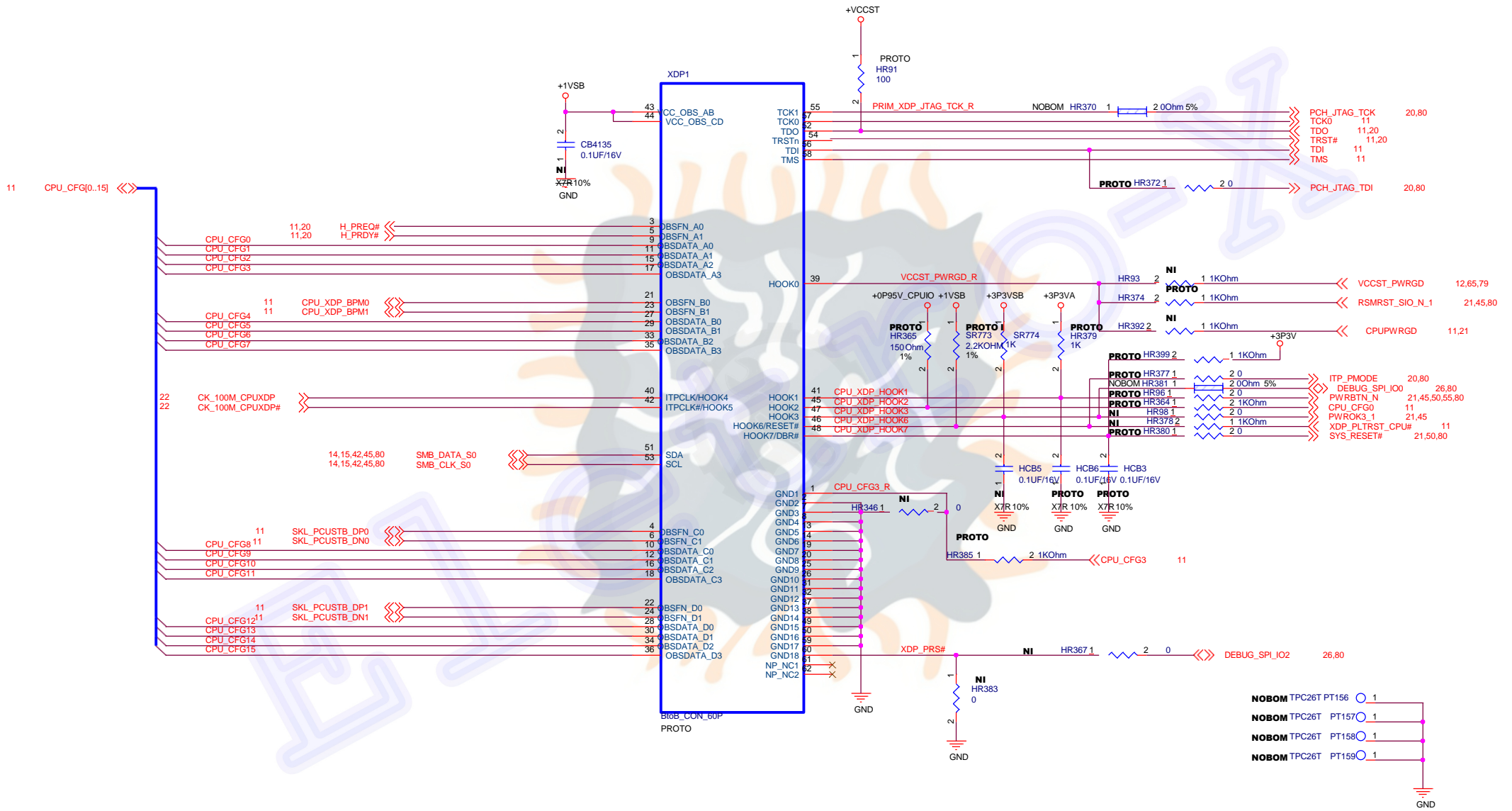


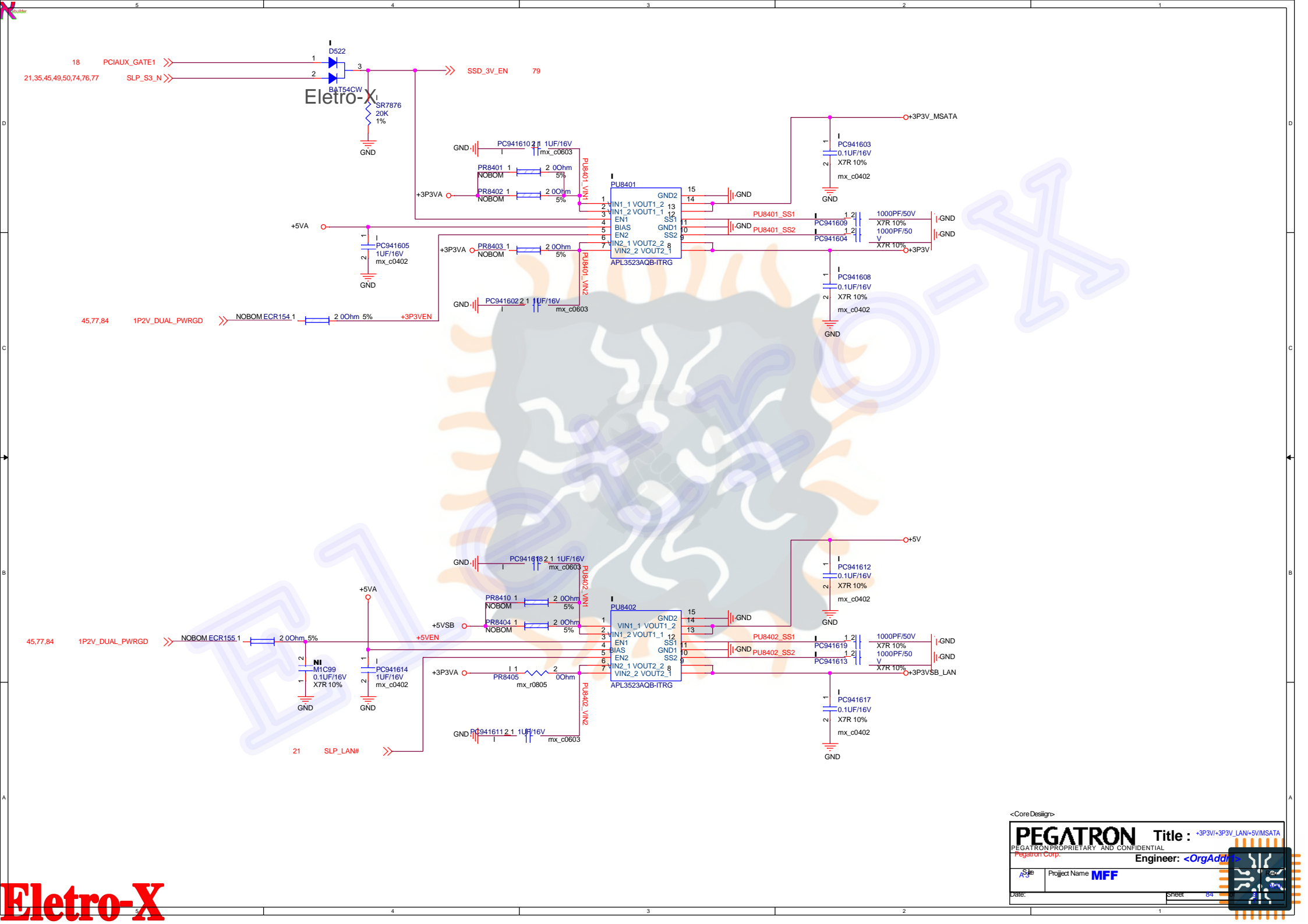


# Eletro-X



# Eletro-X





Eletron-X

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Pegatron Corp.

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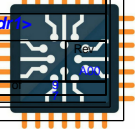
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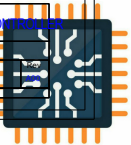
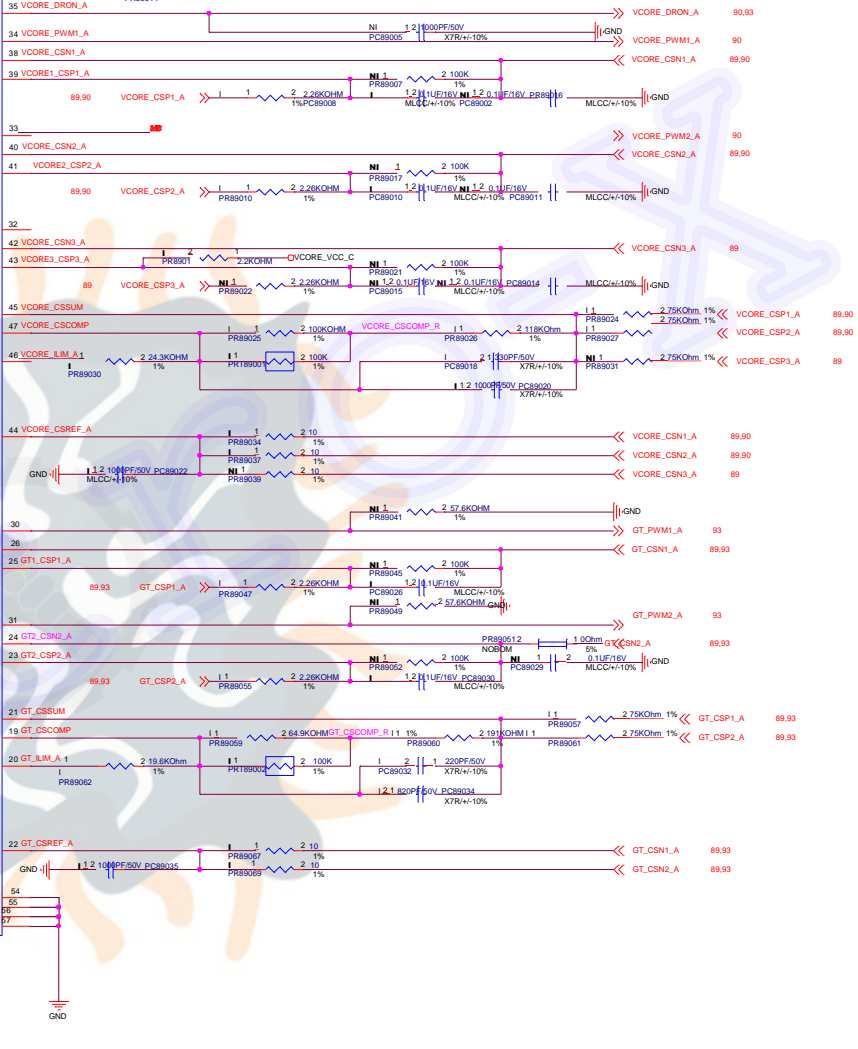
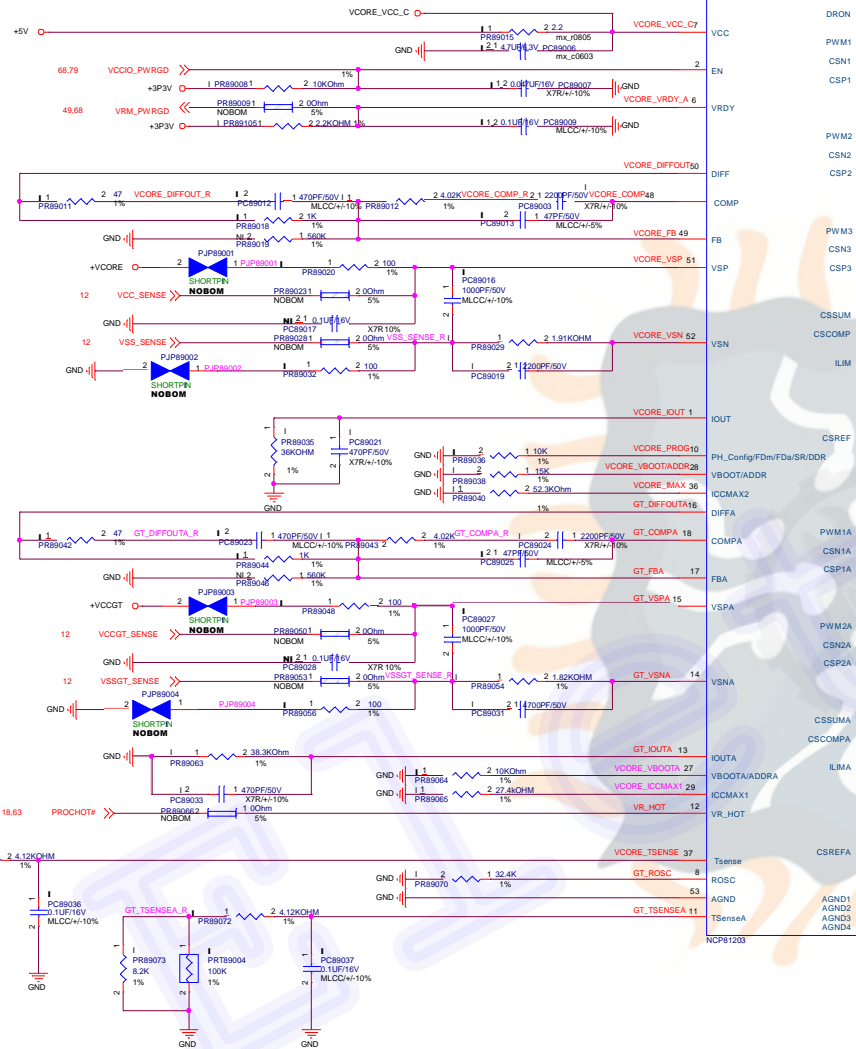
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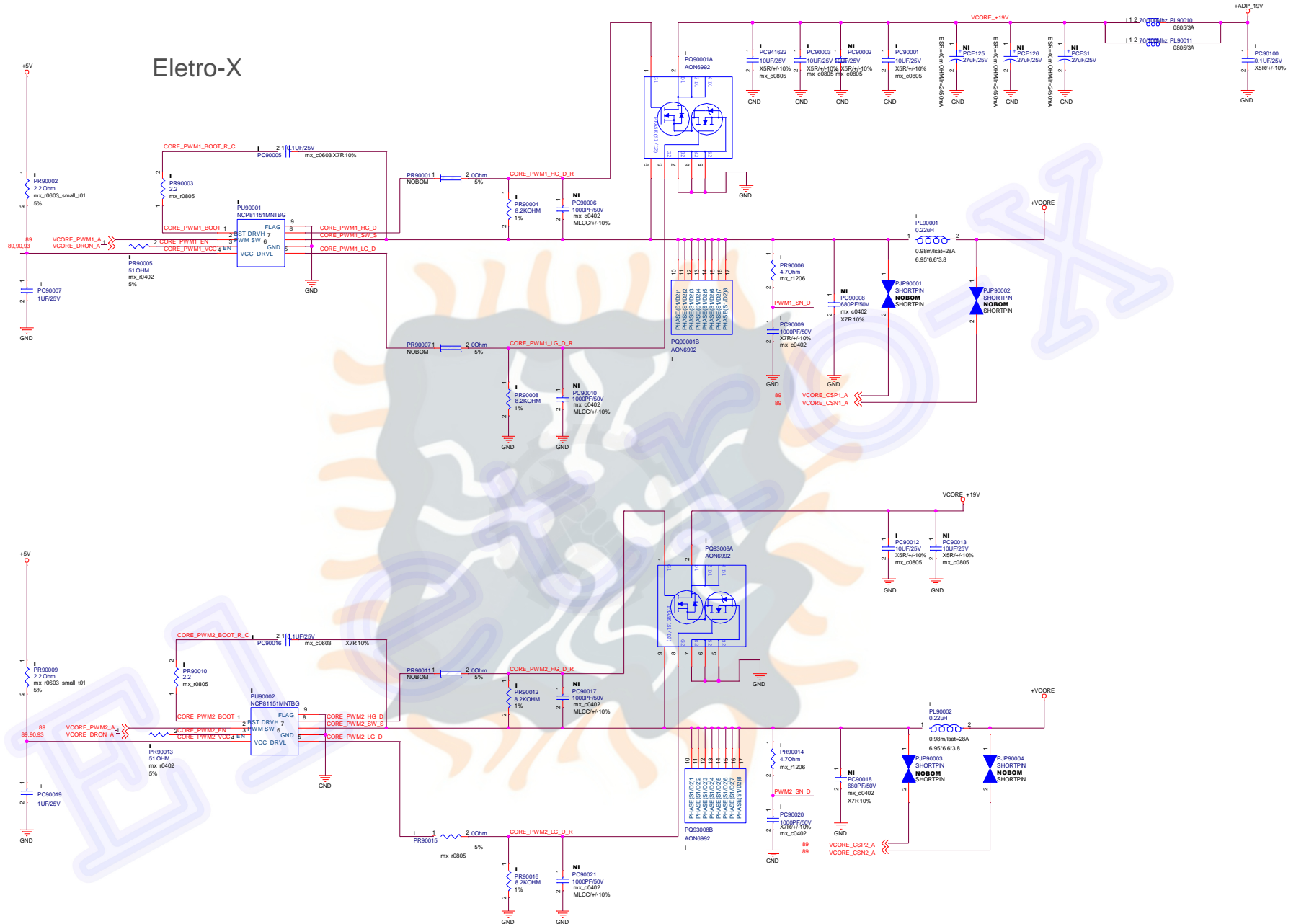
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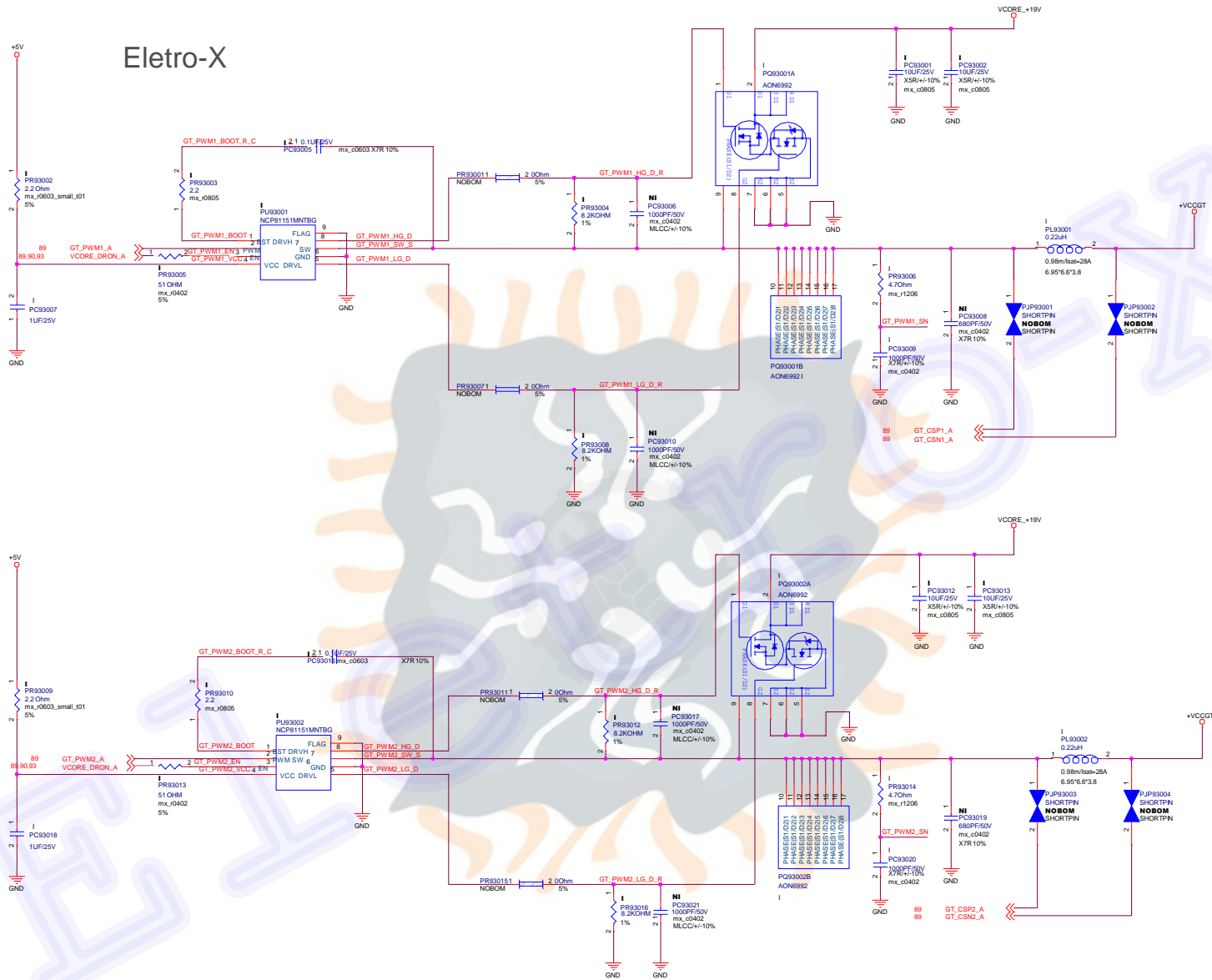
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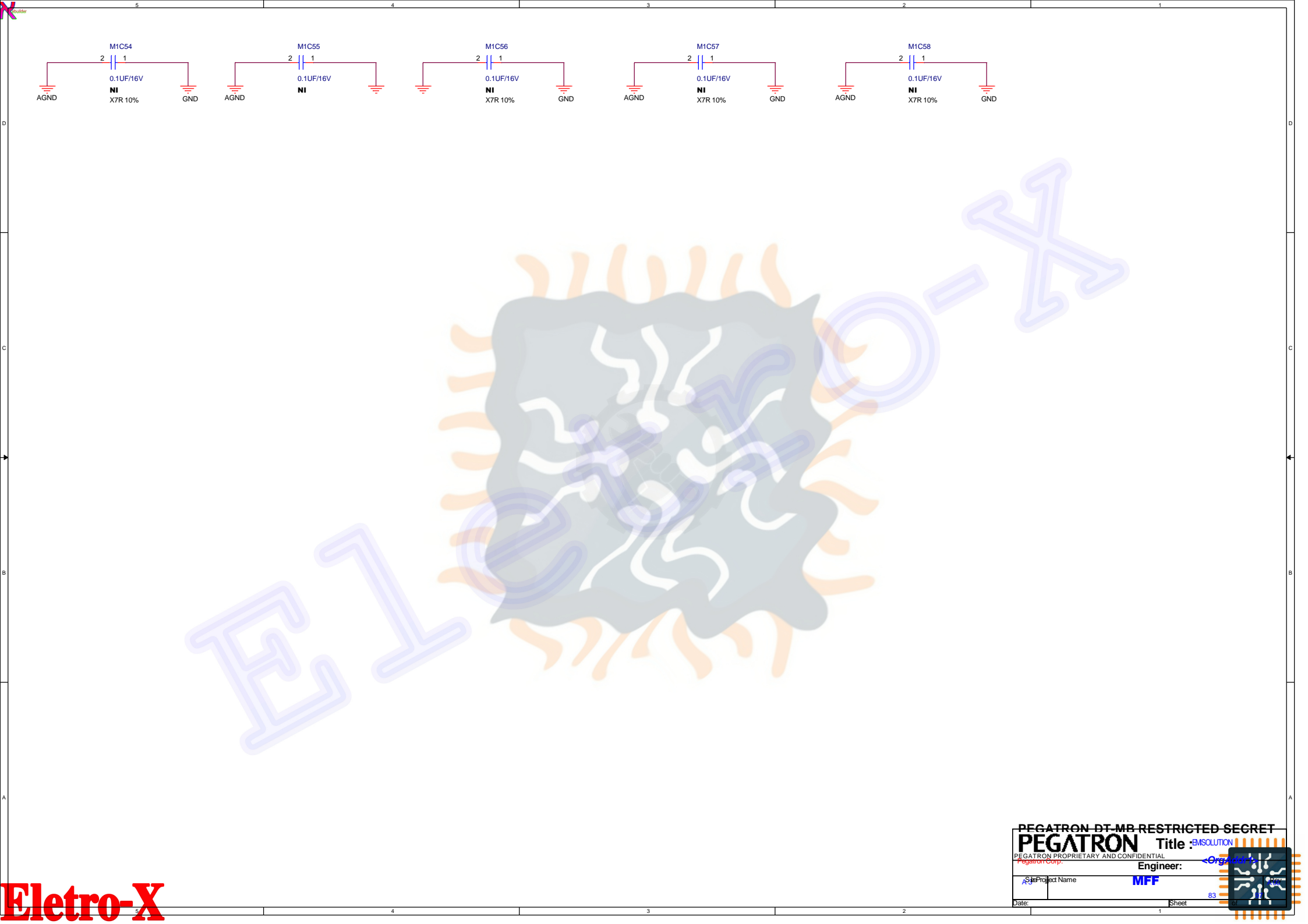
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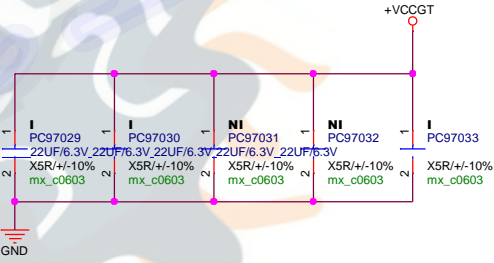
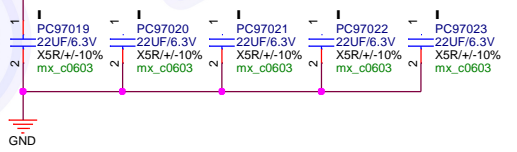
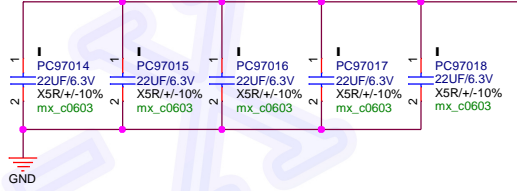
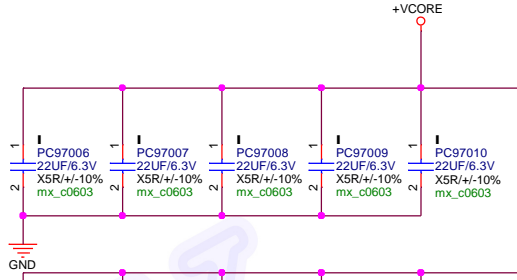
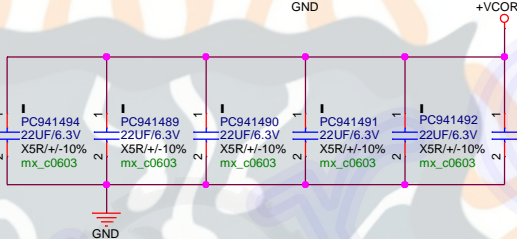
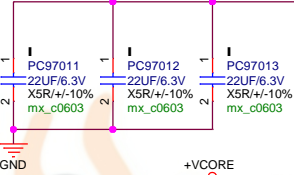
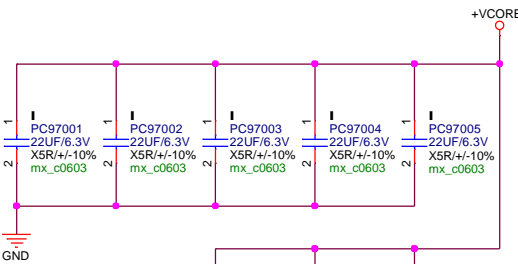
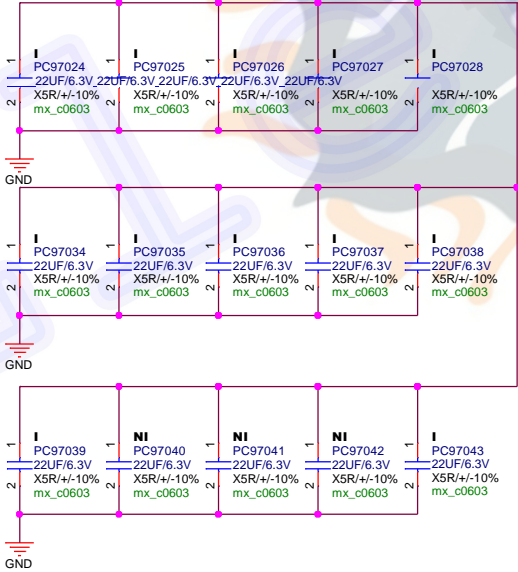
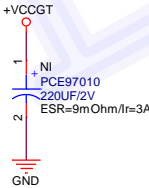
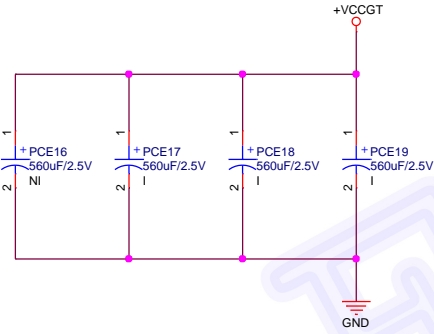
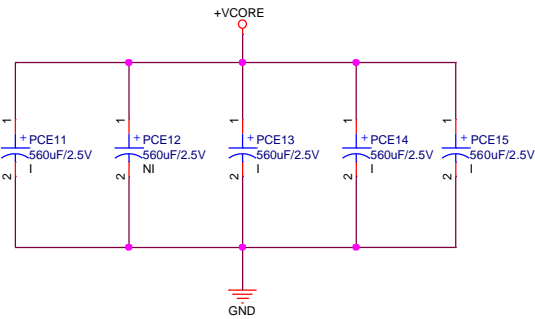
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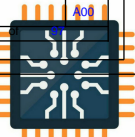
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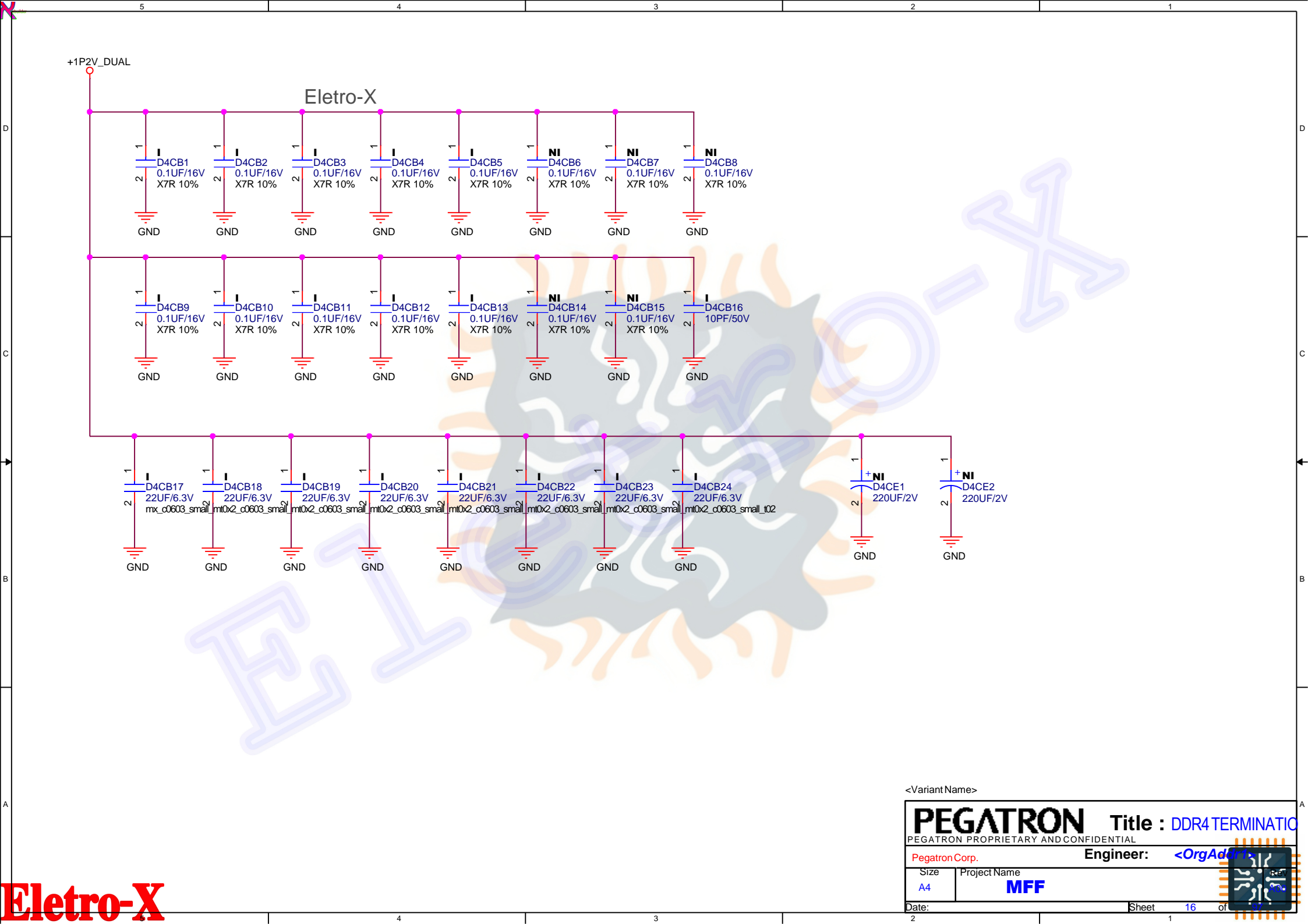
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**PEGATRON**

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Pegatron Corp.

Size  
A4

Date:

Title : **DDR4 TERMINATION**

Engineer: **<OrgAddress>**

Project Name  
**MFF**

Sheet 16 of 16

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